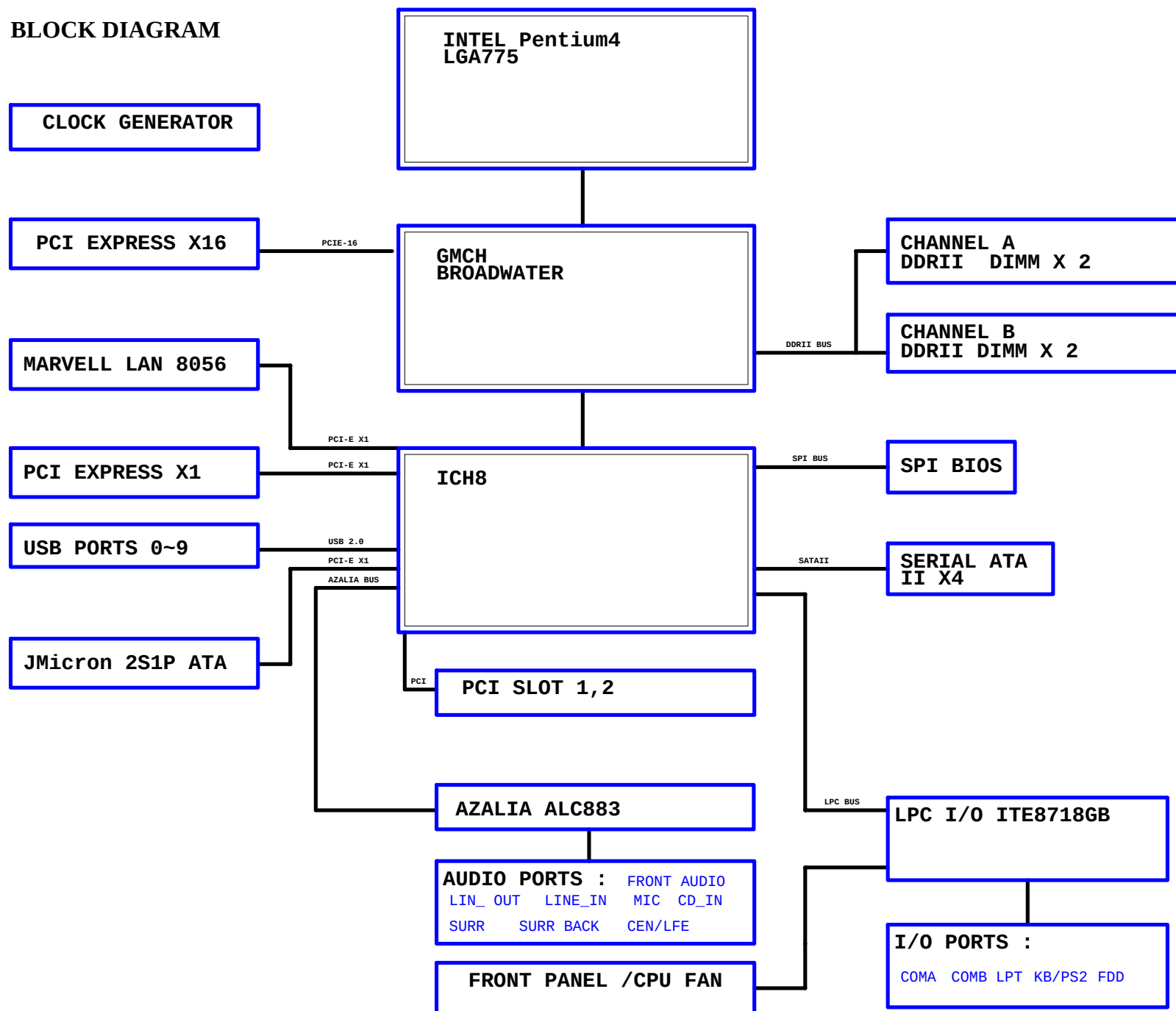


01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	TABLE LIST
05	P4_LGA775_A
06	P4_LGA775_B_D
07	P4_LGA775_C
08	P4_LGA775_E_F_G_H
09	GMCH-BROADWATER_HOST
10	GMCH-BROADWATER_DDRII
11	GMCH-BROADWATER_PCI E, DMI
12	GMCH-BROADWATER_INT VGA
13	GMCH-BROADWATER_GND
14	GMCH-BROADWATER_PWR
15	DDRII CHANNEL A 1,2
16	DDRII CHANNEL B 1,2
17	DDRII TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH8 PCI, USB, DMI, LAN
20	ICH8 GPIO, CTRL
21	ICH8 SATA, FAN PWM
22	ICH8 VCC, GND
23	CLK GEN IC5LP505-2HGLFT
24	PCI EXPRESS*1 ,PCI SLOT 1,2
25	ITE8718GB
26	COM, LPT, -PROH0T
27	BIOS , HWM , KB/MS ,CI

28	AZALIA ALC888
29	AUDIO JACK
30	VCORE PWM ISL6312
31	DISCRETE POWER
32	ATX POWER
33	FRONT PANEL ,FUSB,FDD
34	NINEVEH LAN
35	JMicron 368
36	REAR USB , TPM
37	

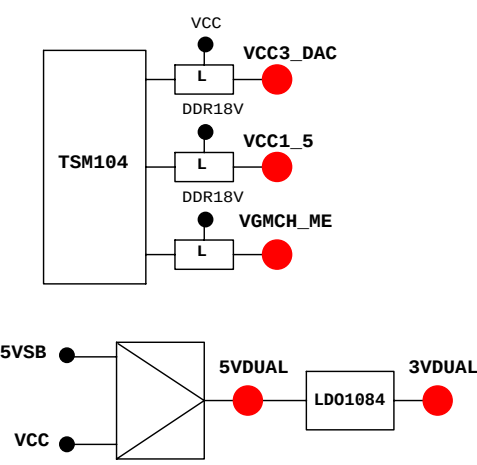
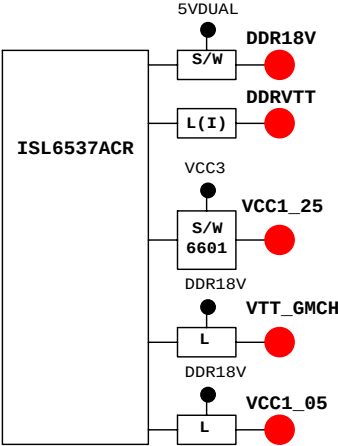
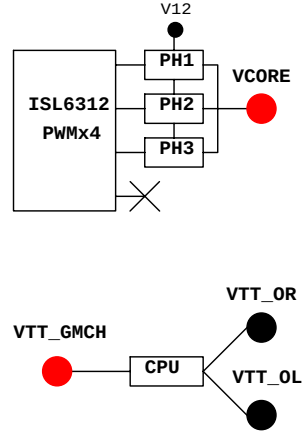
BLOCK DIAGRAM



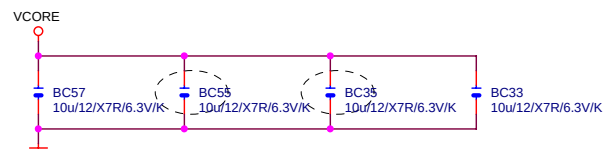
ICH8 GPIO LIST TABLE

PIN NAME	PWR WELL	AFTER/ PLTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	N/A	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	N/A	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	N/A	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	OUT	GPIO10	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID3	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	GPIO14	P/U 8.2K 3VDUAL
GP15	STBY	OUT	GPIO15(TP)	N/A
GP16	MAIN	IN	MB_ID1	P/U 8.2K VCC3
GP17/TACH0	MAIN	IN	N/A(OPT-ITE)	P/U 8.2K VCC3
GP18	MAIN	IN	MB_ID2	P/U 8.2K VCC3
GP19	MAIN	IN	GPIO19	P/U 8.2K VCC3
GP20	MAIN	OUT	GPIO20	N/A
GP21	MAIN	IN	GPIO21	P/U 8.2K VCC3
GP22	MAIN	IN	GPIO22	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	GPIO24(TP)	N/A
GP25	STBY	IN	MB_ID4	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	GPIO26	P/U 8.2K 3VDUAL
GP27	STBY	LOW	GPIO27	ENERGY-LAKE LED:GREEN
GP28	STBY	LOW	GPIO28	ENERGY-LAKE LED:YEL
GP29/OC5#	STBY	IN	-USB0C_F	P/U FUSEVCC1
GP30/OC6#	STBY	IN	-USB0C_F	P/U FUSEVCC1
GP31/OC7#	STBY	IN	-USB0C_F	P/U FUSEVCC1
GP32	MAIN	IN	MB_ID0	P/U 8.2K VCC3
GP33	MAIN	IN	GPIO33	P/U 8.2K VCC3
GP34	MAIN	IN	GPIO34	P/U 8.2K VCC3
GP35	MAIN	IN	N/A(TP)	N/A
GP36	MAIN	IN	GPIO36	P/U 8.2K VCC3
GP37	MAIN	IN	GPIO37	P/U 8.2K VCC3
GP38	MAIN	IN	GPIO38	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

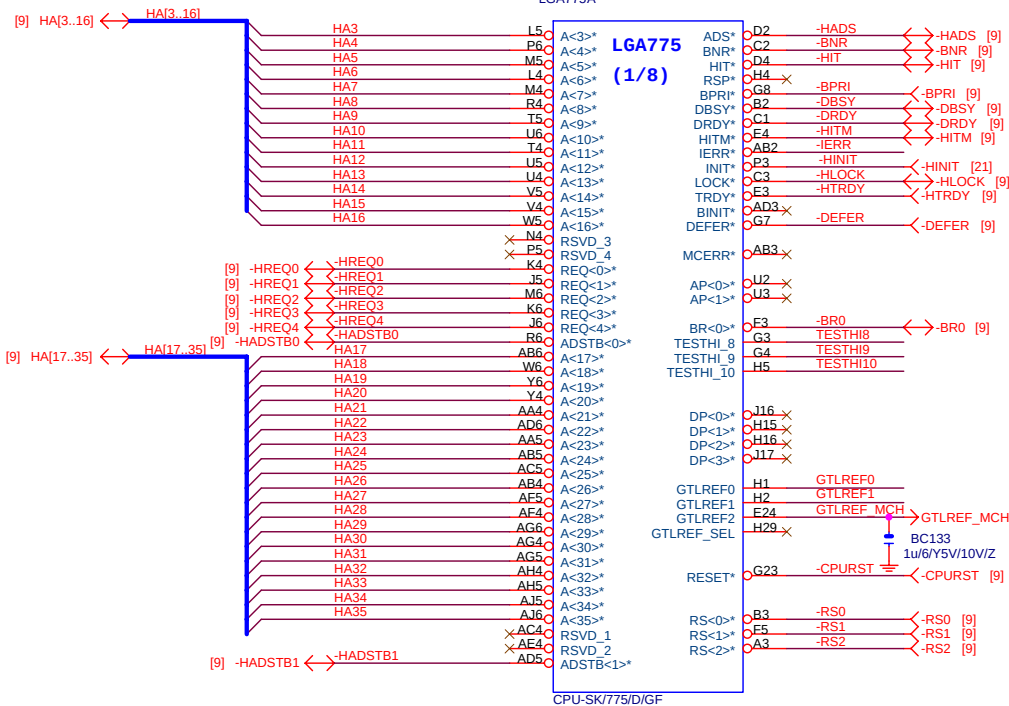
VCORE:3 PHASE PWM--ISL6312



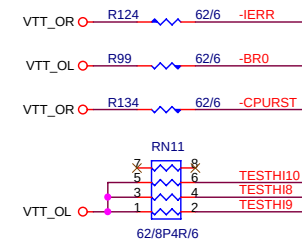
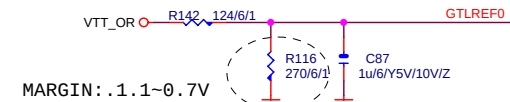
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TABLE LIST			
Size B	Document Number		Rev
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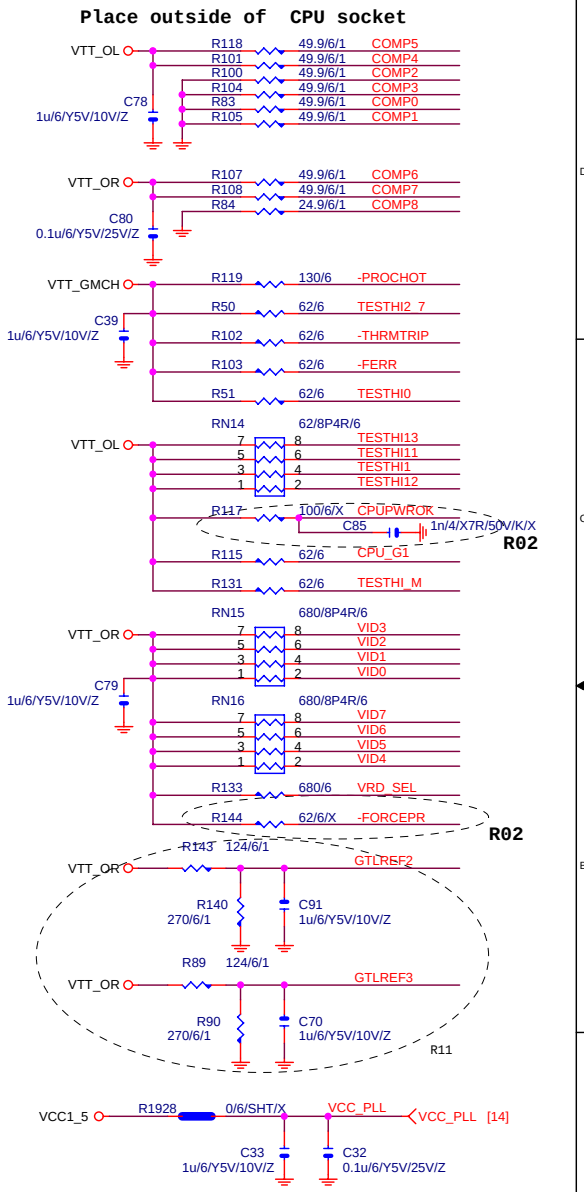
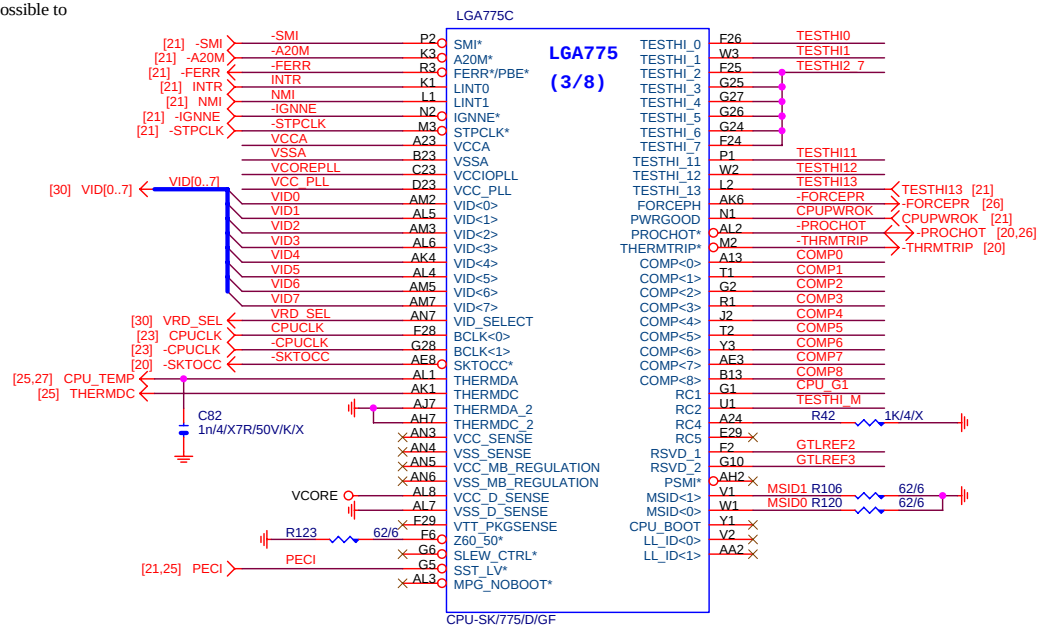
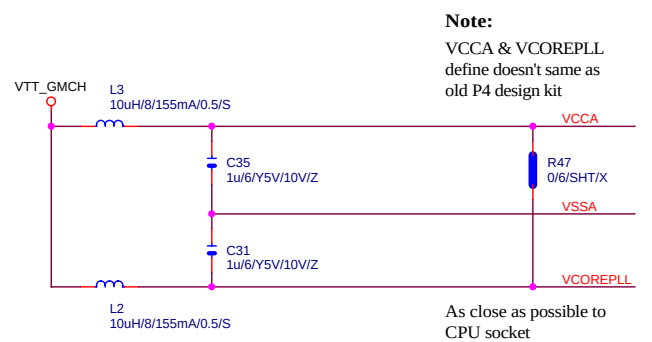


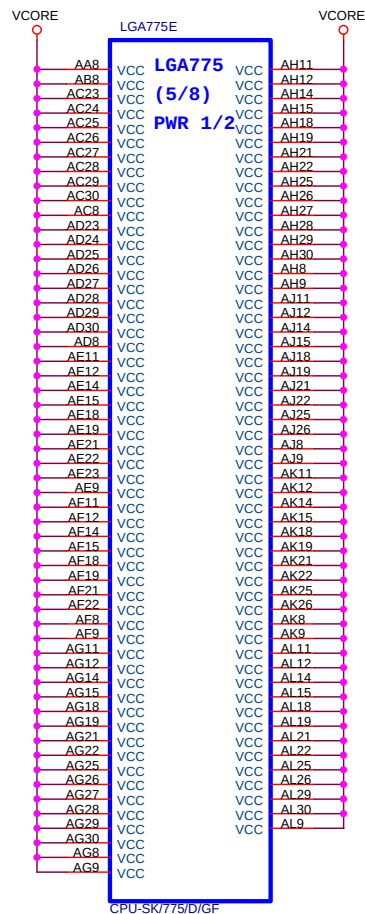
LGA775A



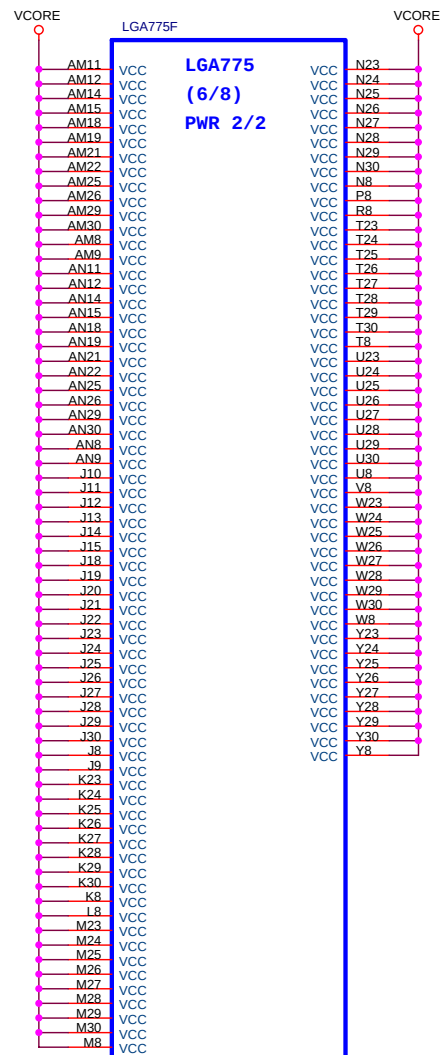
A diagram showing a square frame. Inside the square is a large circle that is tangent to all four sides of the square. In each of the four corners of the square, there is a smaller circle. Each of these four small circles is tangent to two sides of the square and also tangent to the large central circle.



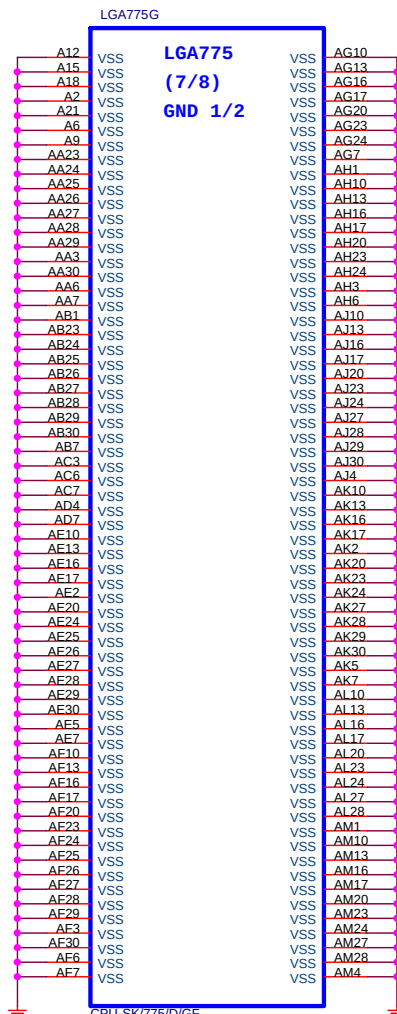




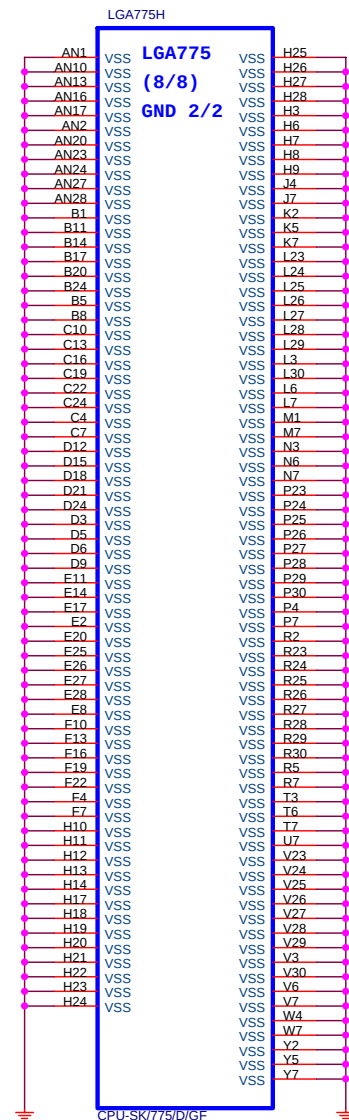
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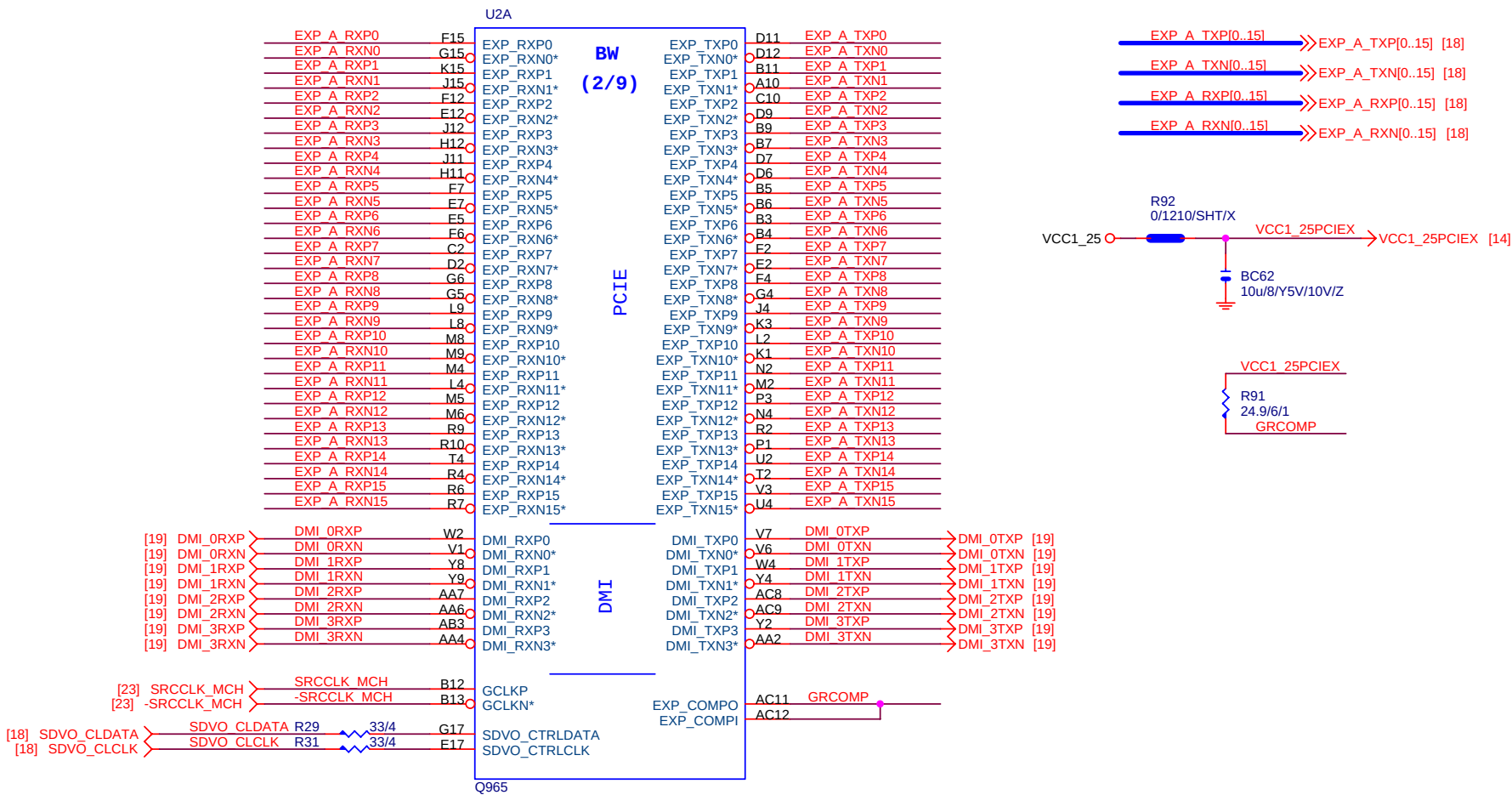
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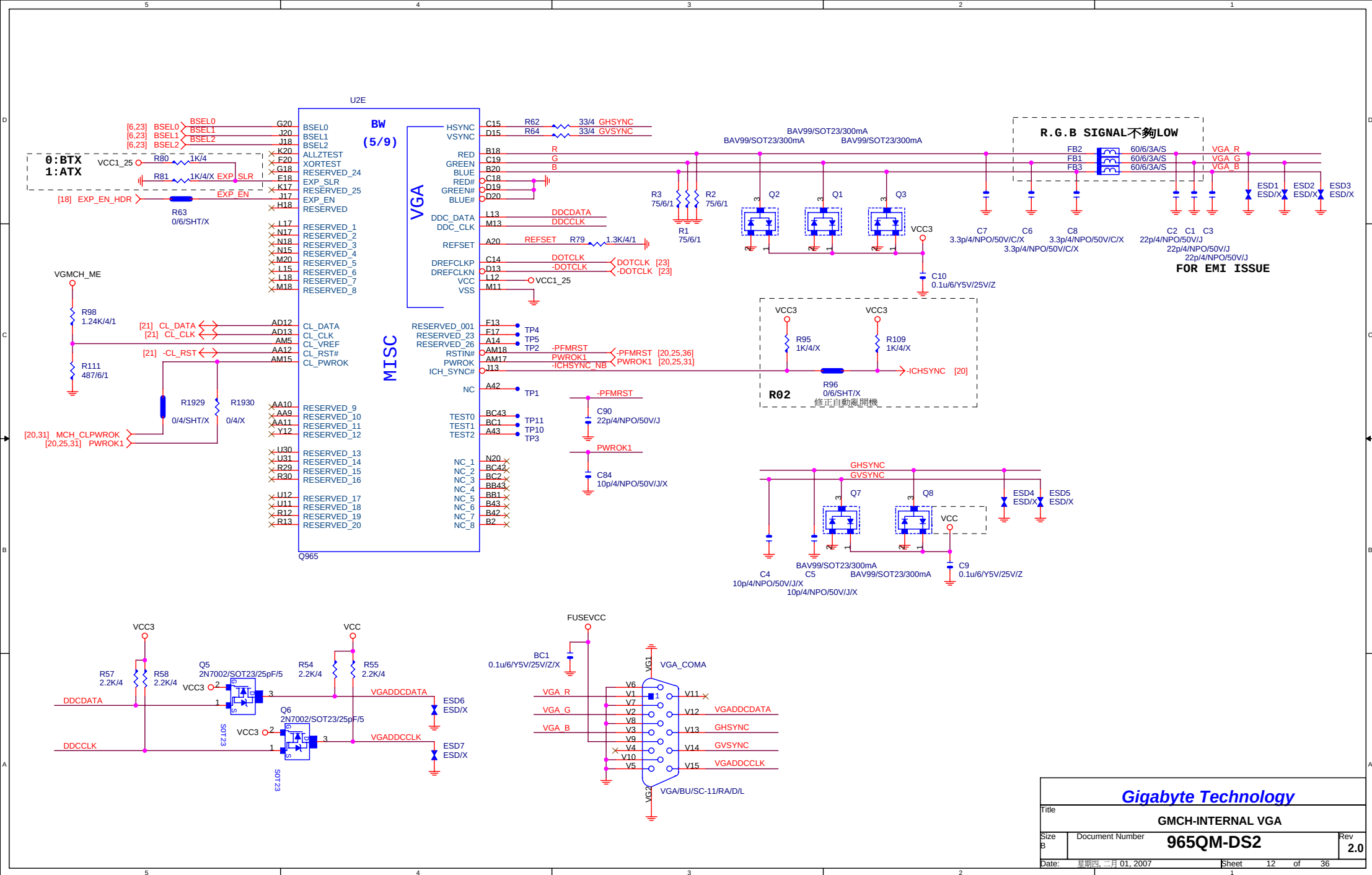


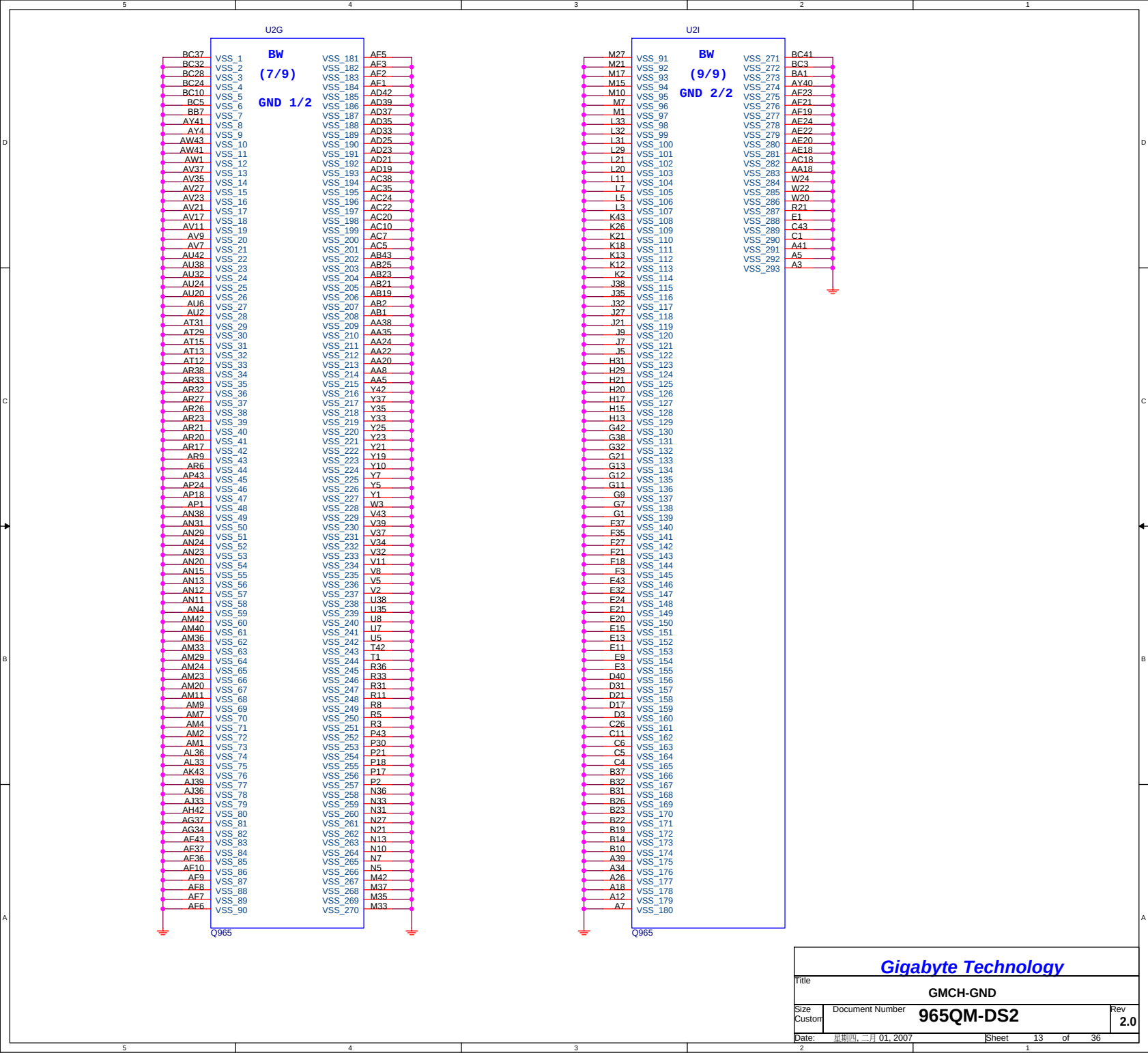
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CPU-SK/775/D/GF

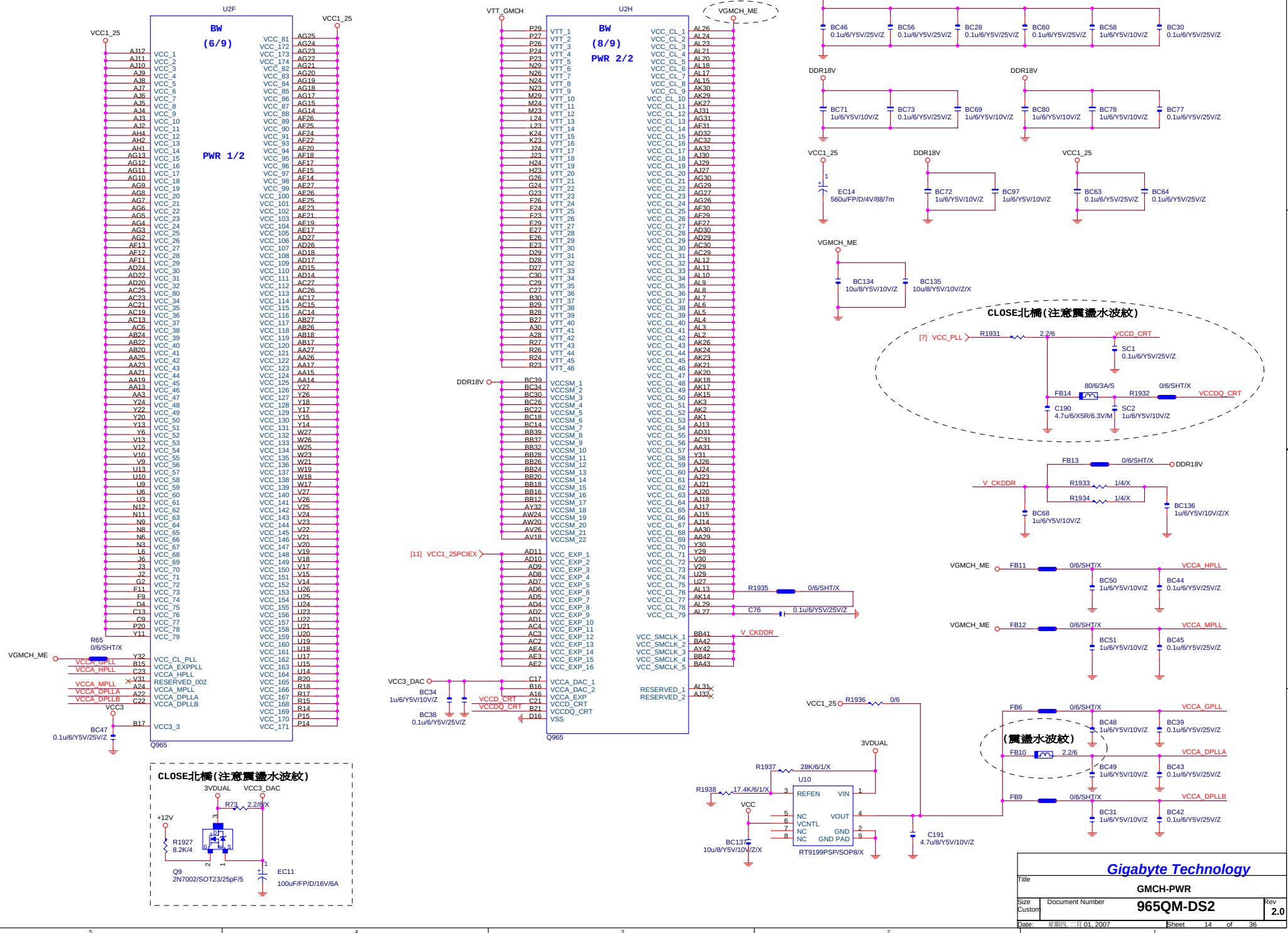


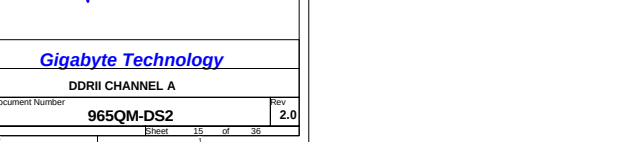
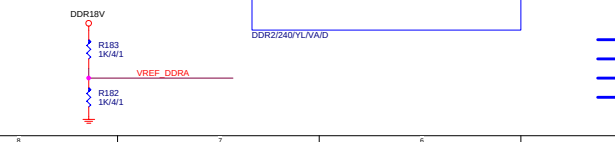


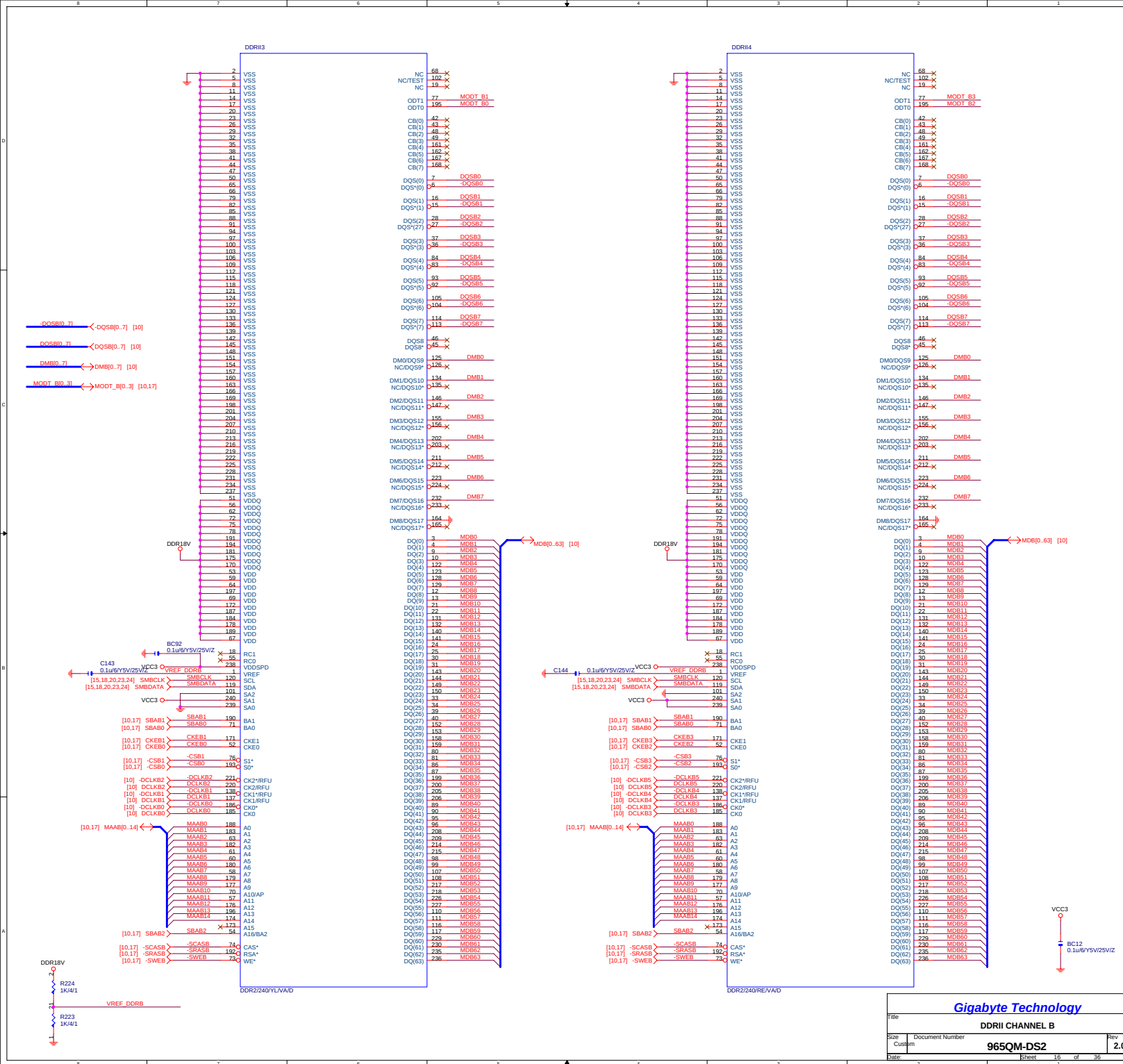


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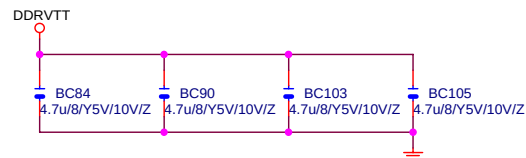
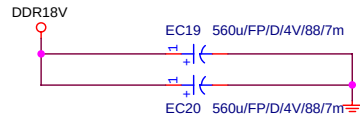




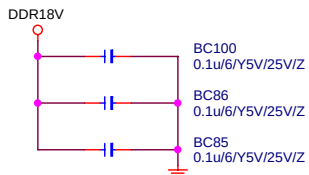


DDR TERMINATION CHANNEL A

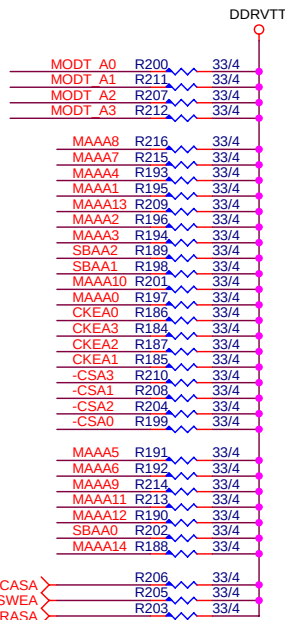
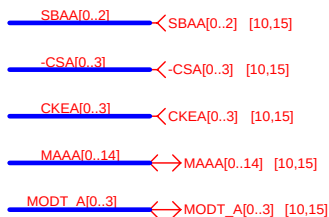
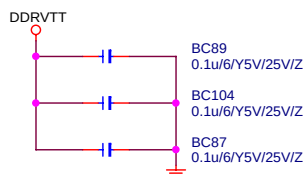
DDRVTT Decouple



DDR18V Decouple

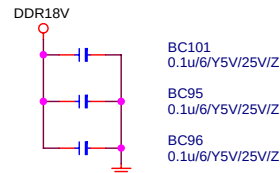


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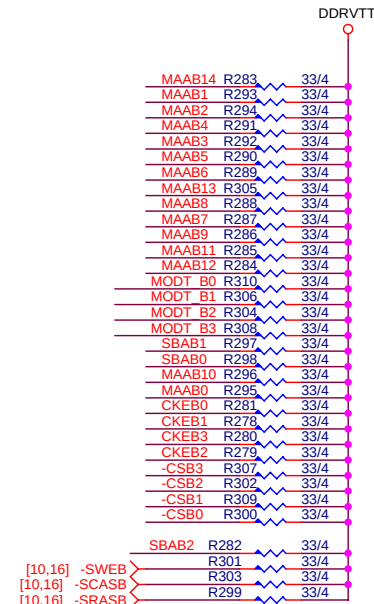
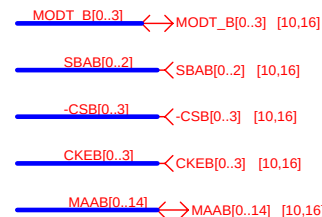
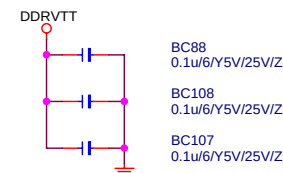


DDR TERMINATION CHANNEL B

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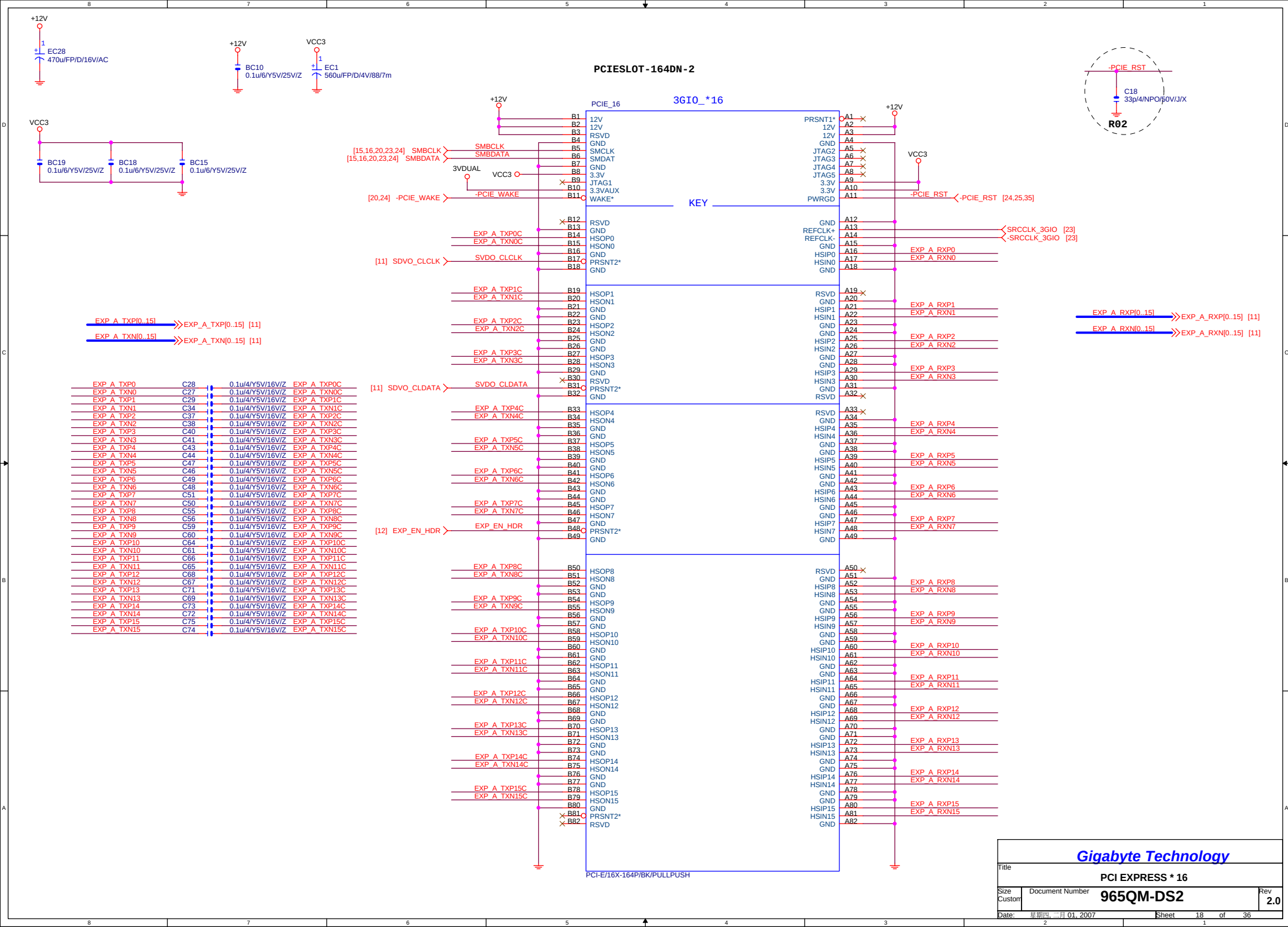


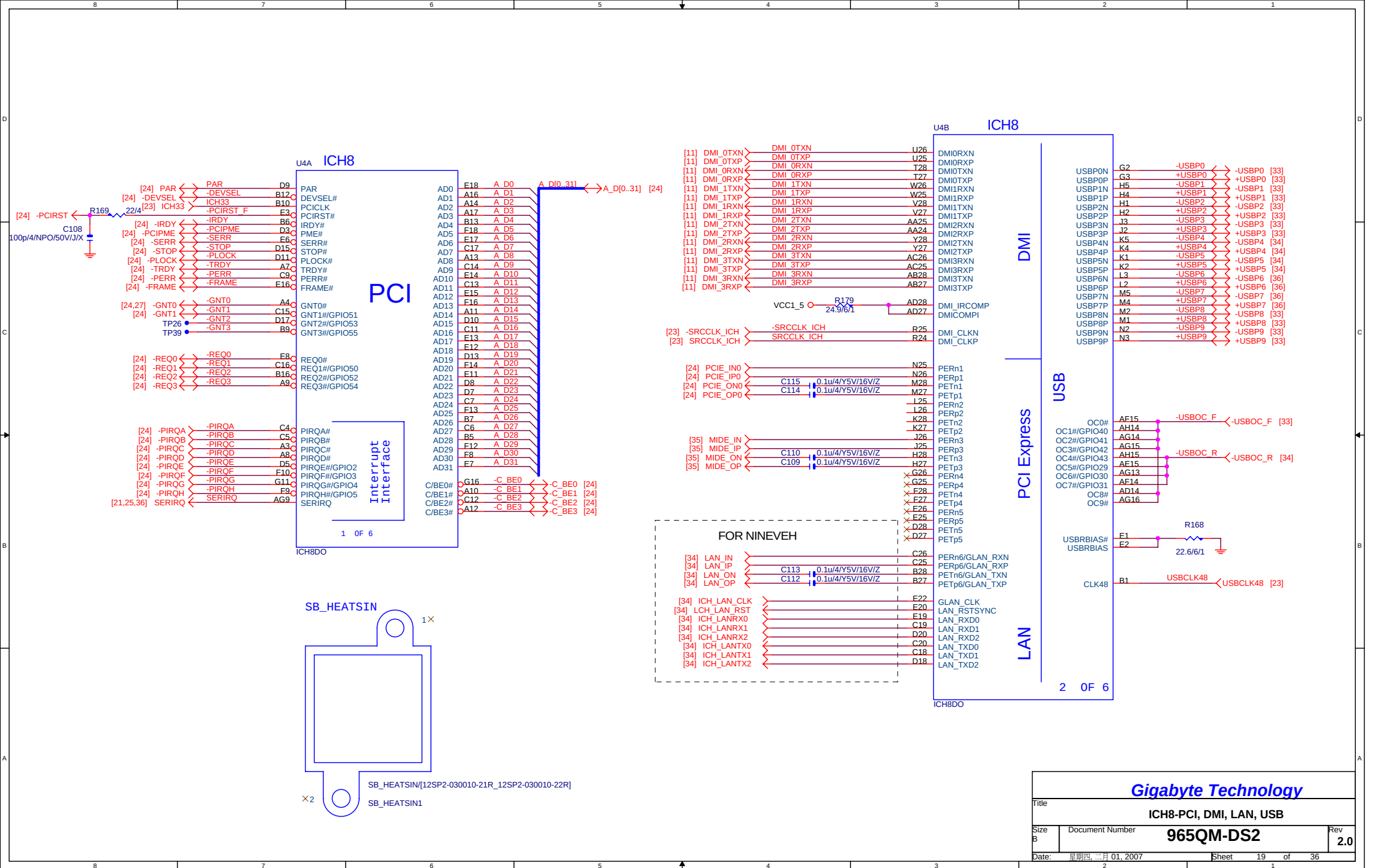
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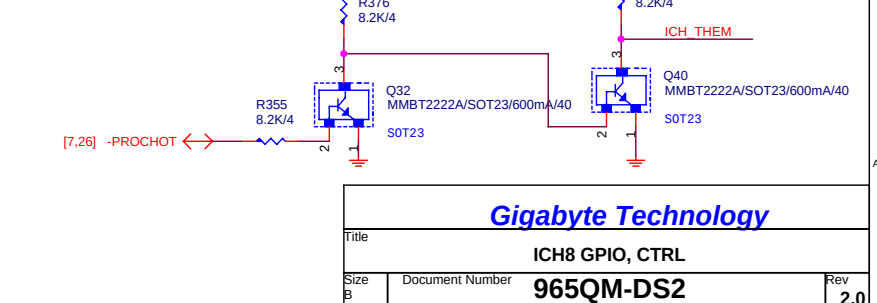
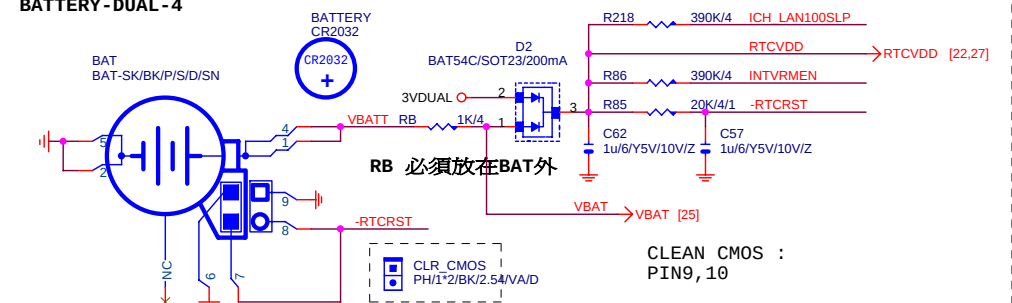
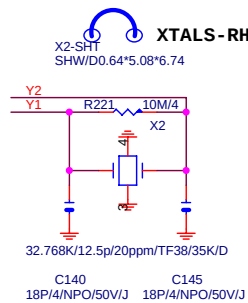
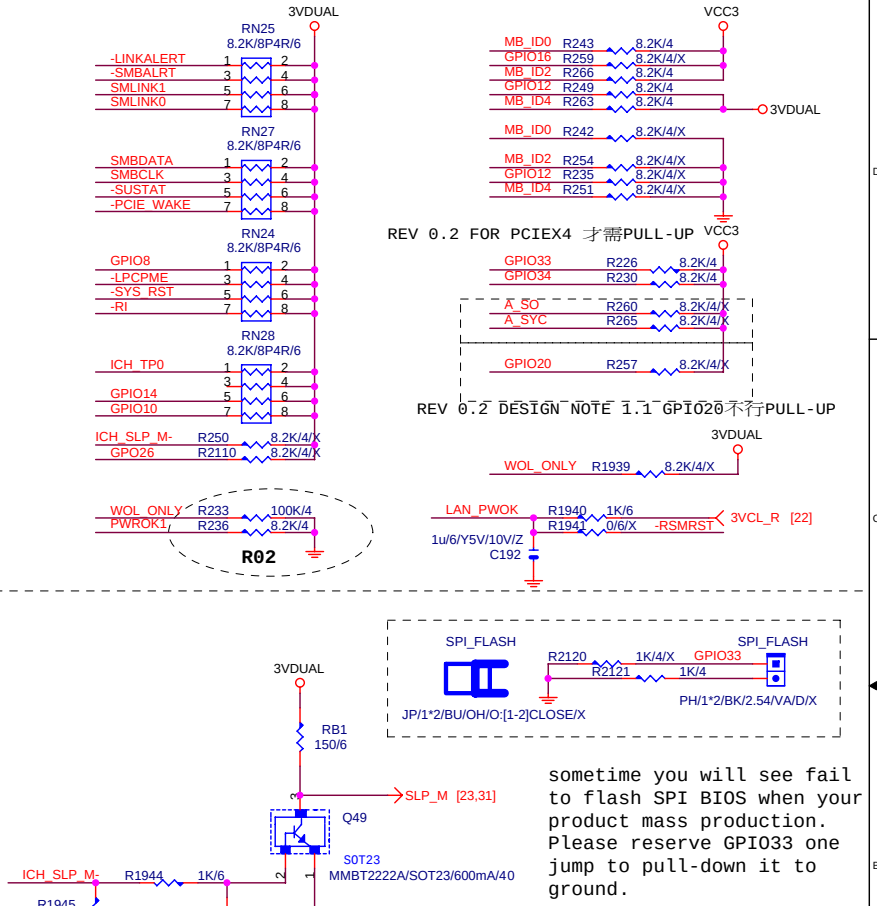
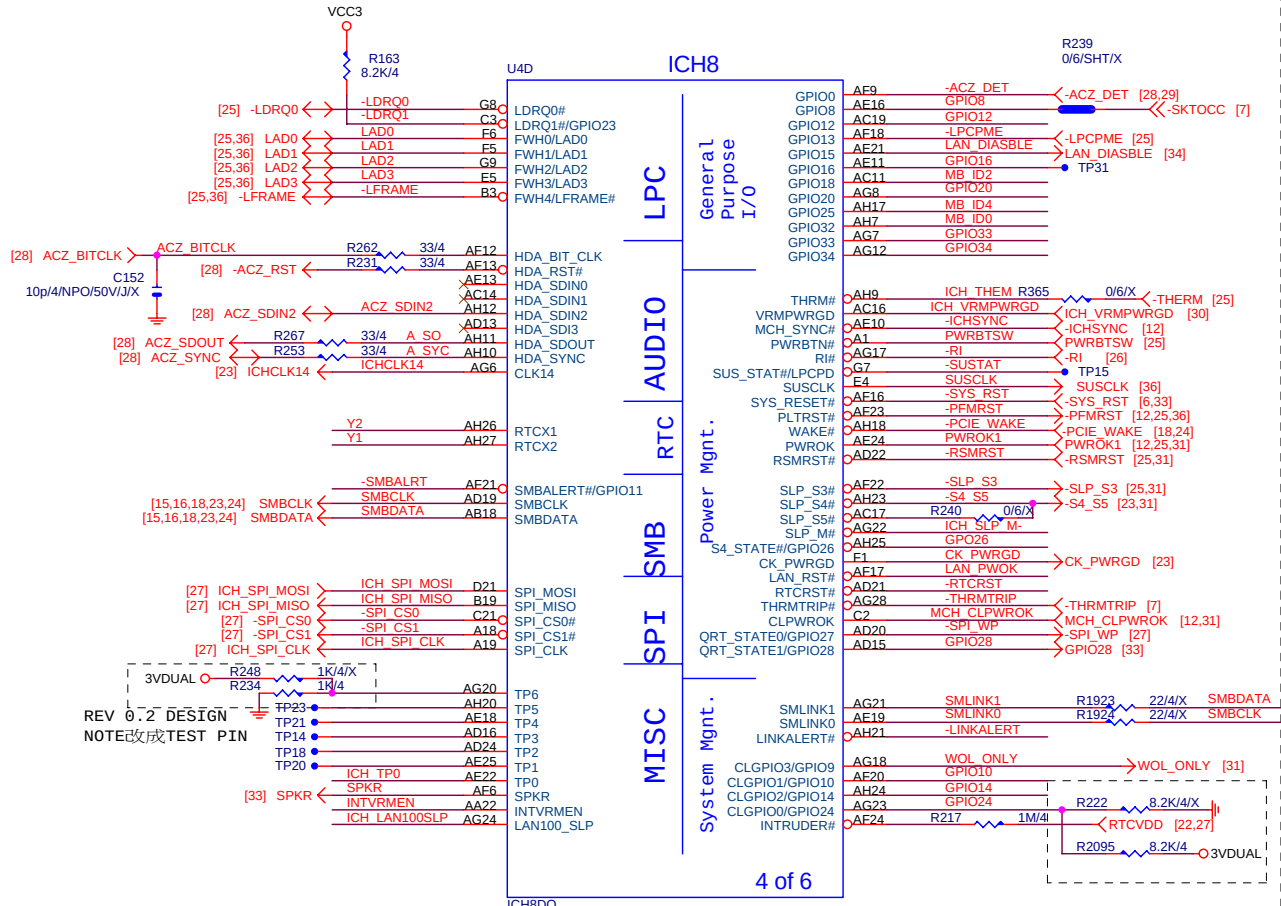


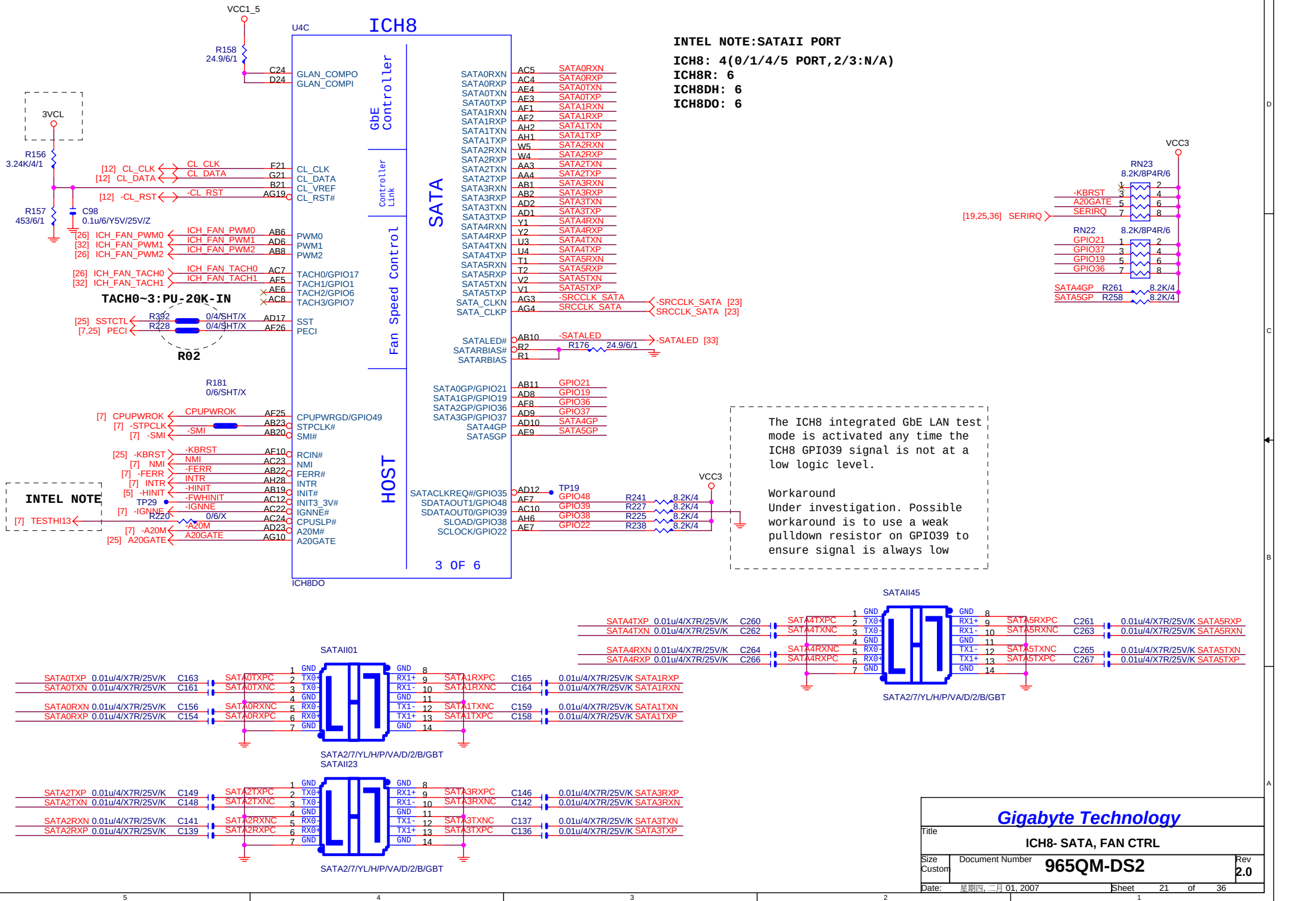
Gigabyte Technology

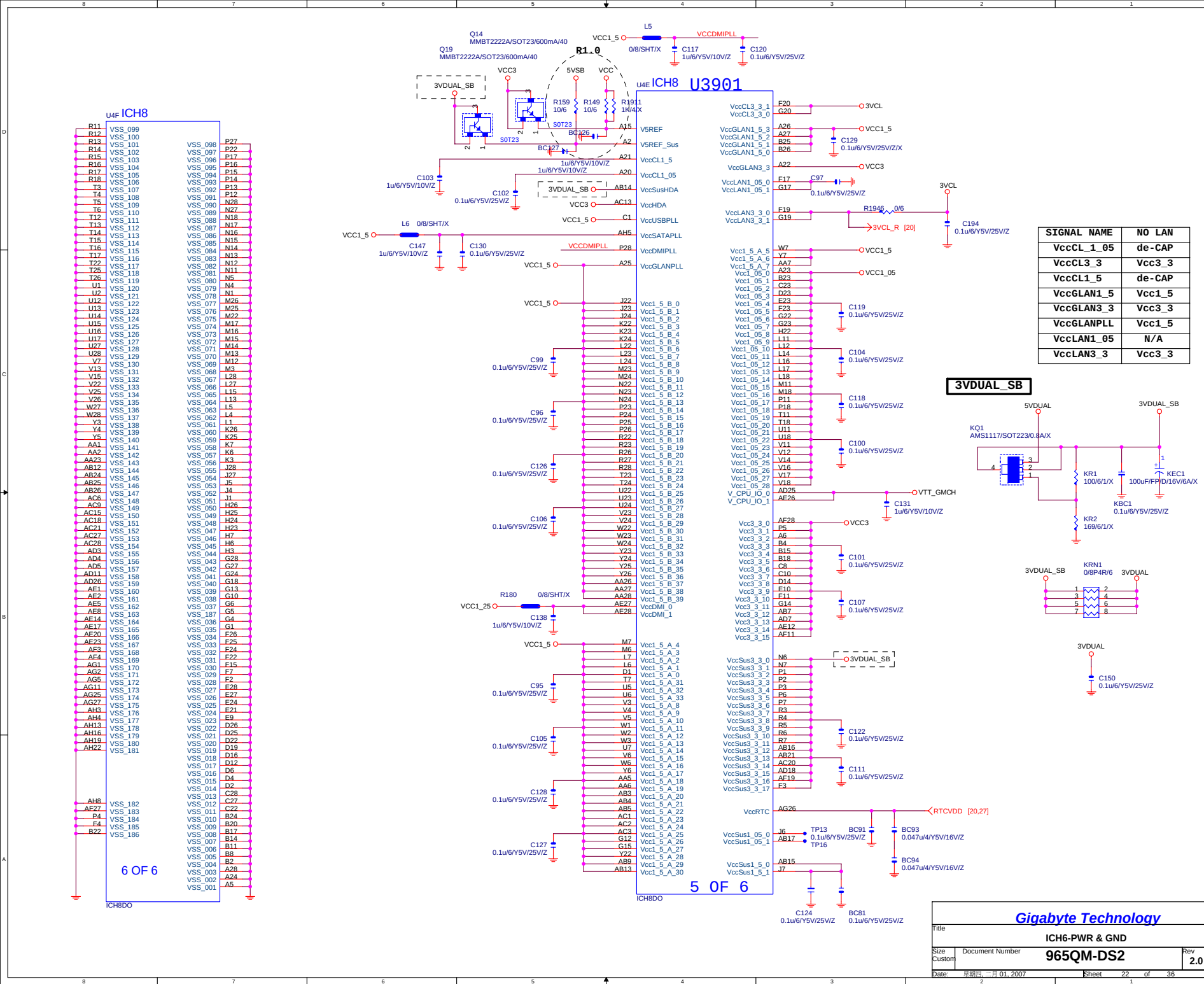
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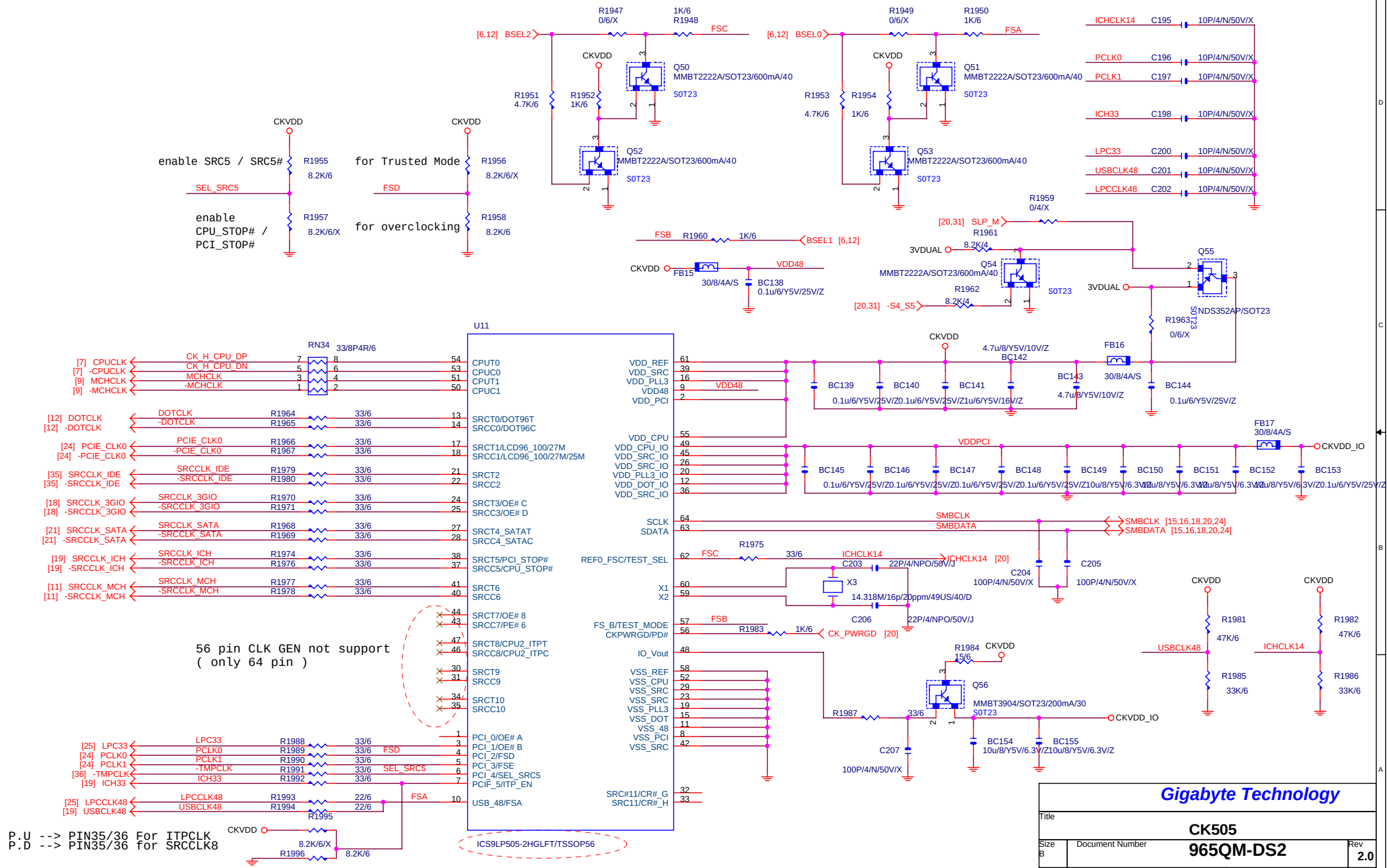


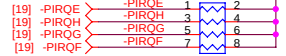
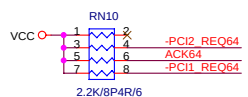




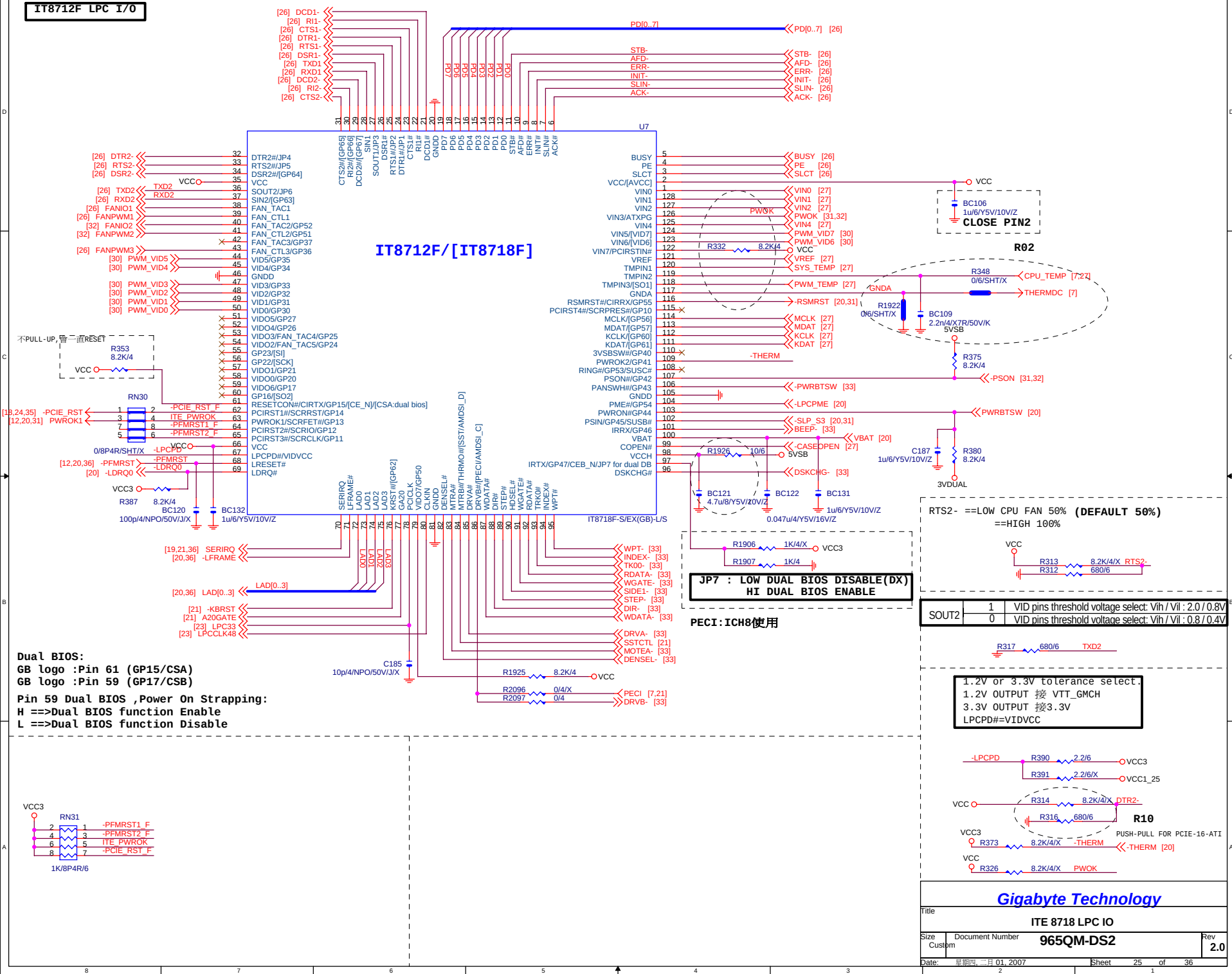




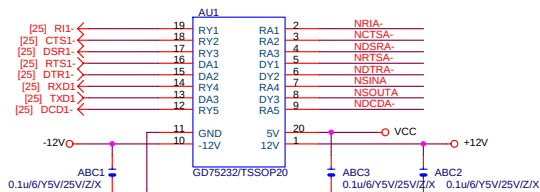




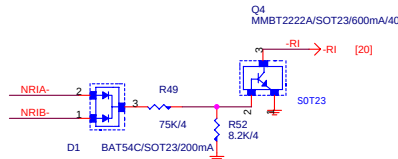
IT8712F LPC I/O



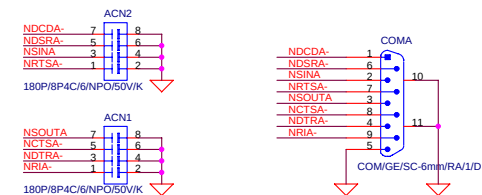
COMA



COM RI

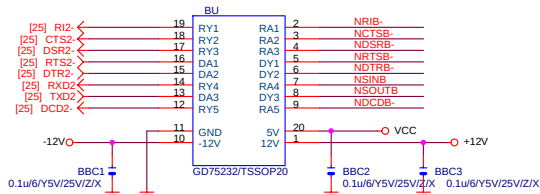


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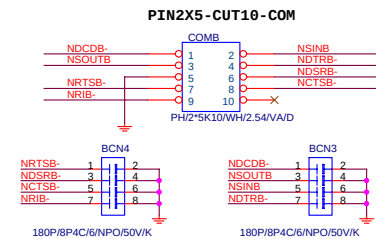


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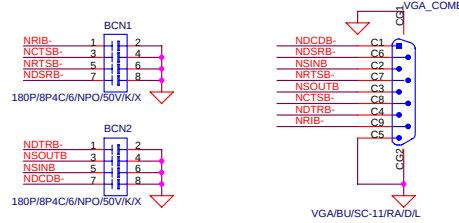
COMB



INTERNAL COMB

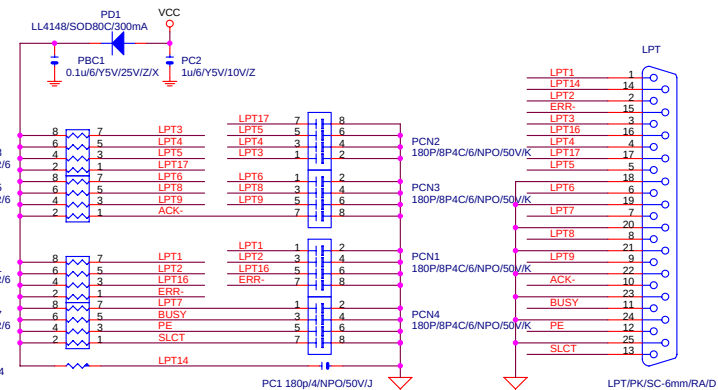
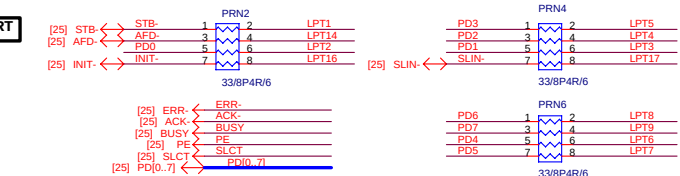


EXTERNAL COMB

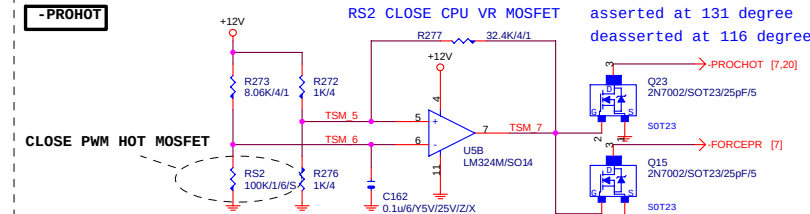


PLACE NEAR VGA_COM CONNECTOR

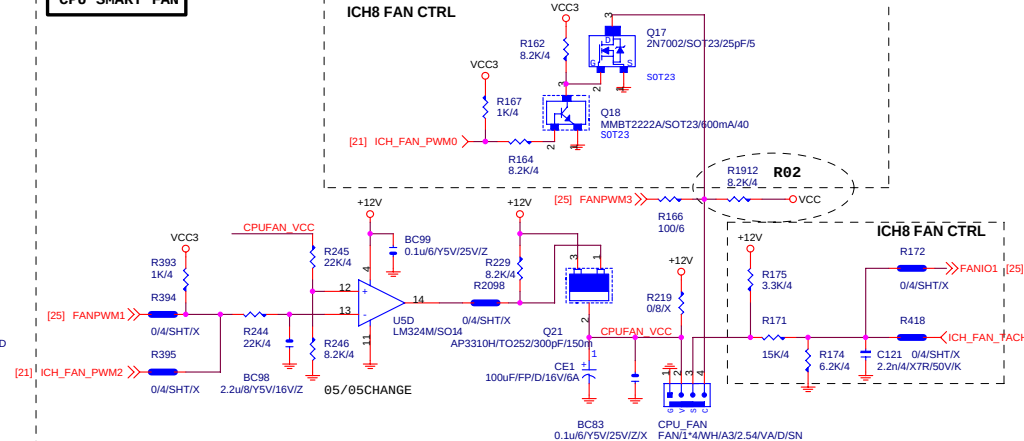
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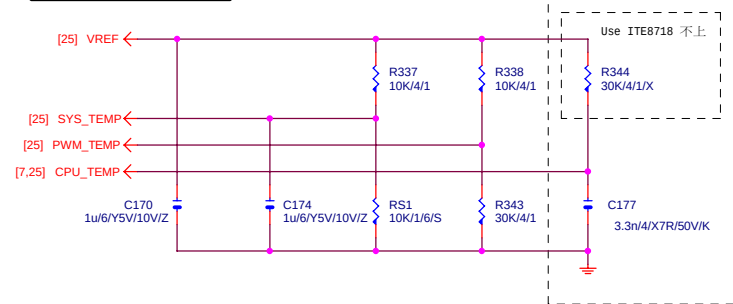
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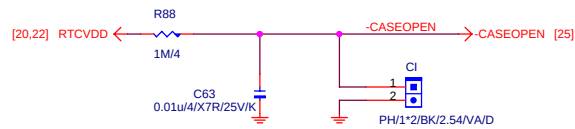
CPU SMART FAN



TEMP H/W MONITOR

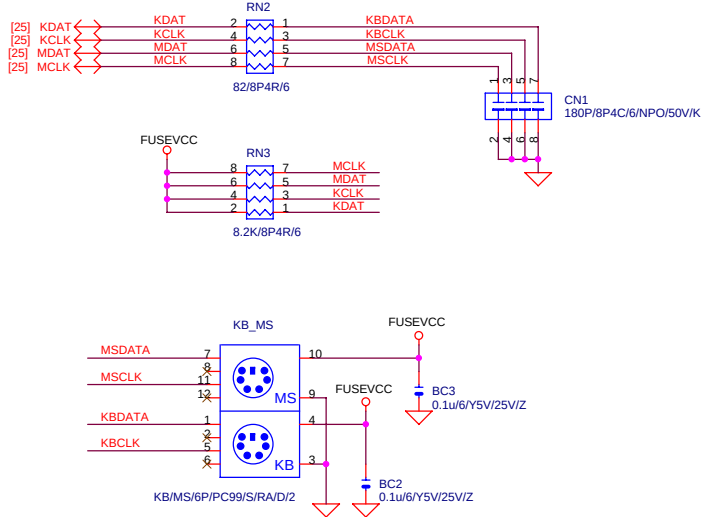


CASE OPEN

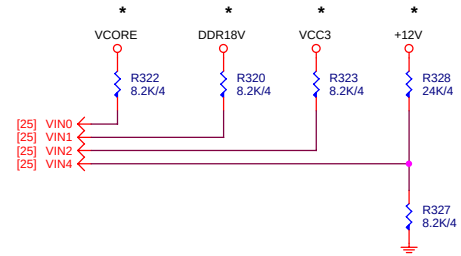


Case Open Circuits

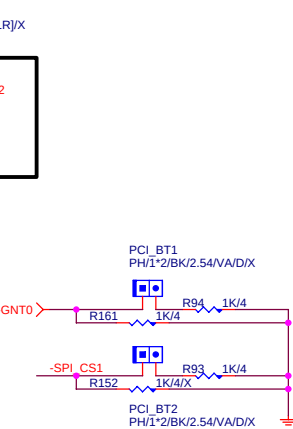
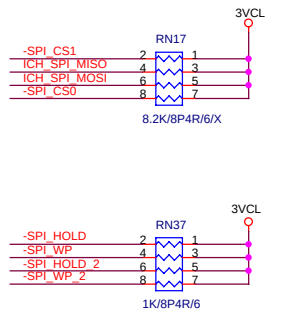
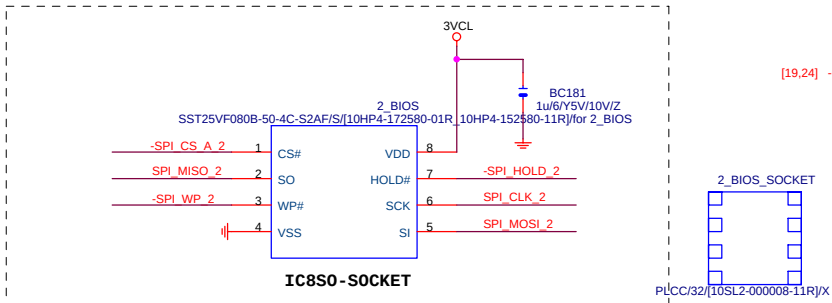
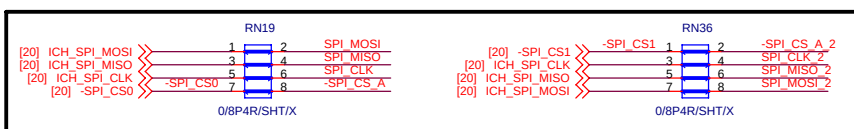
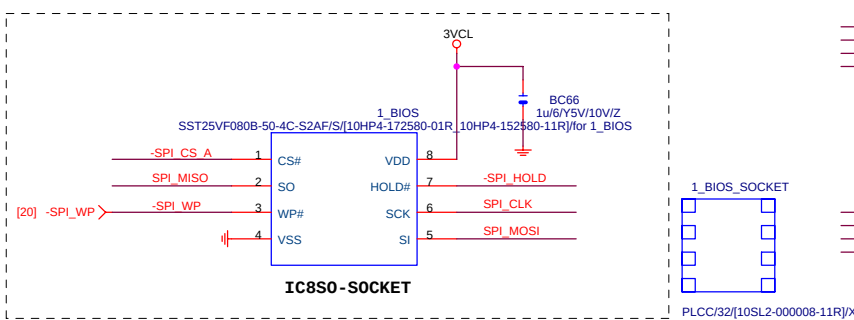
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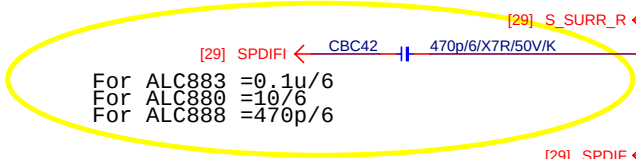
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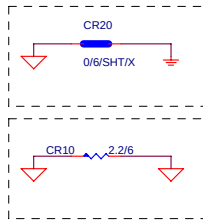
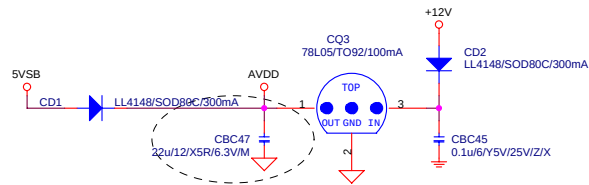
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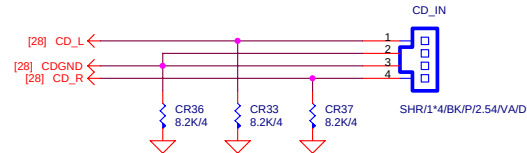
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PCI	1	0
FWH	1	1



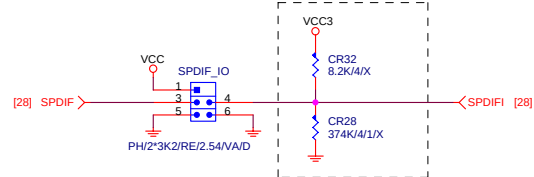
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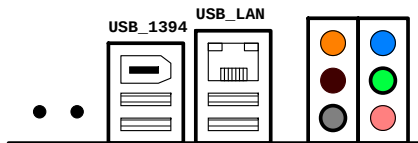
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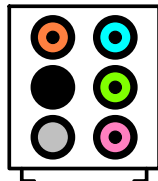
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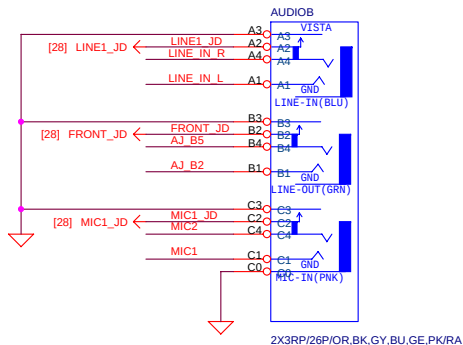
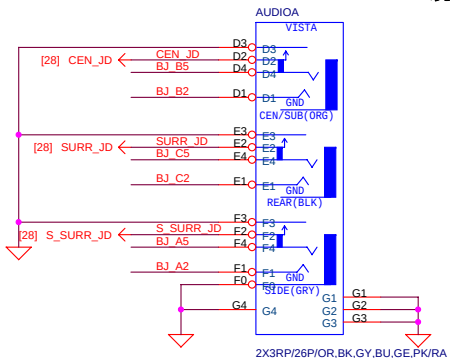
AZALIA JACK



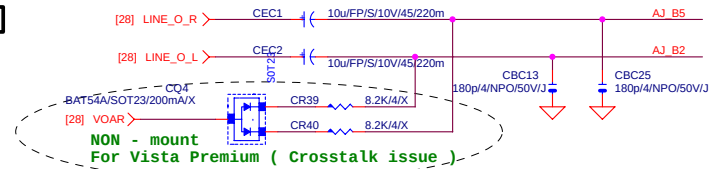
BTX AZALIA CONNECTOR



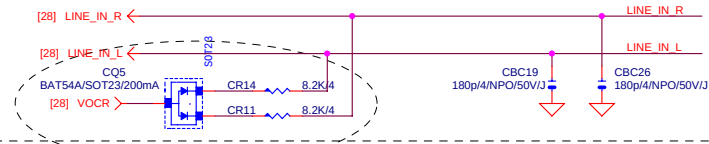
3RP/26P/OR, BK, GY, BU, GE, PK/RA/D/1/B
VISTA規範: REAR-->BLK, CEN/SUB-->ORG



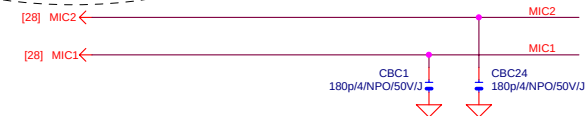
LINE-OUT



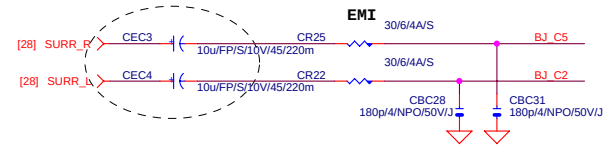
LINE-IN



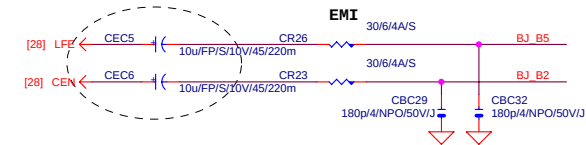
MIC-IN



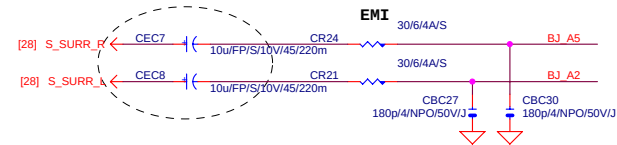
SURROUND



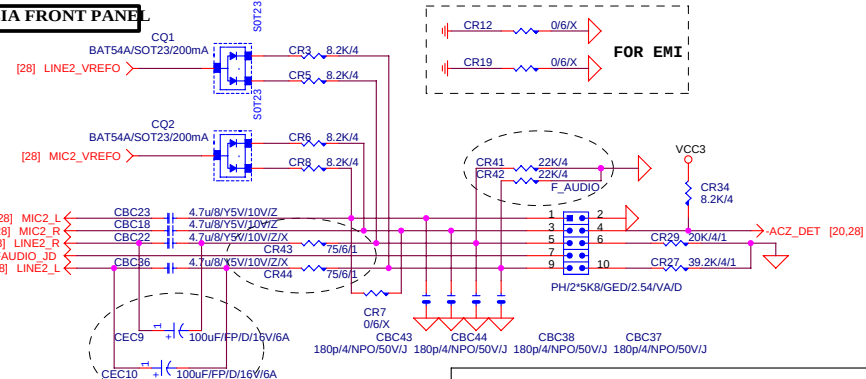
CEN/LFE



SURR BACK

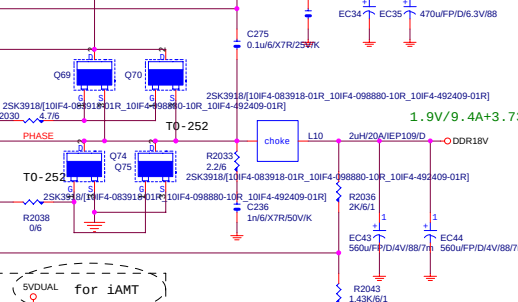
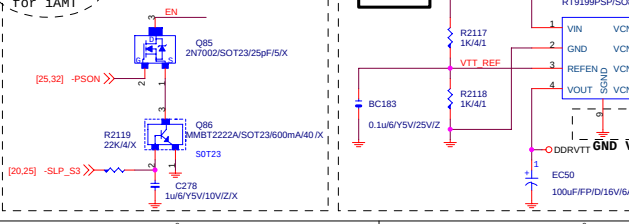
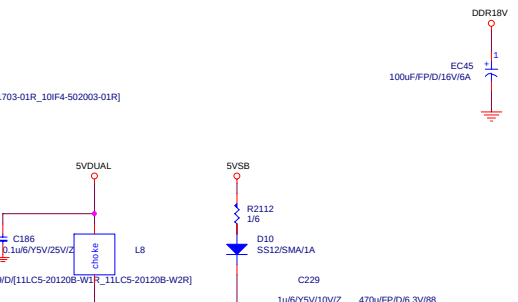
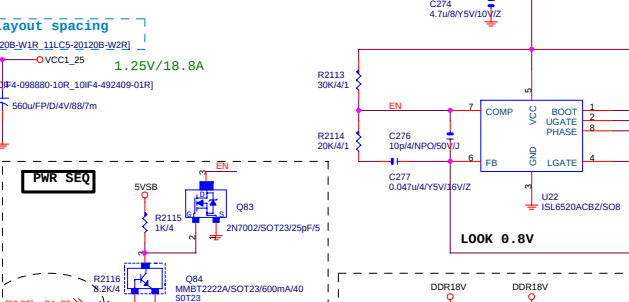
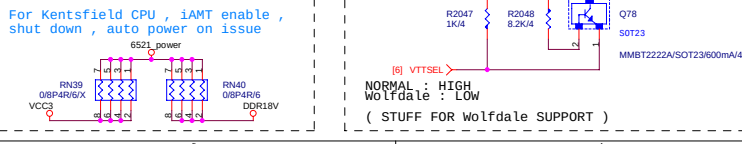
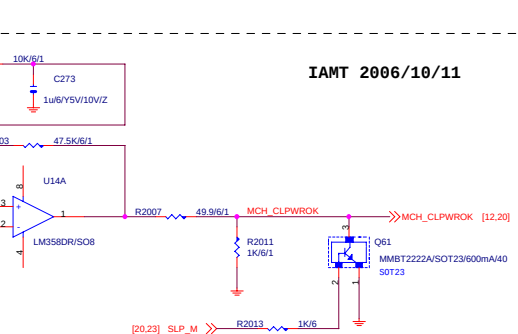
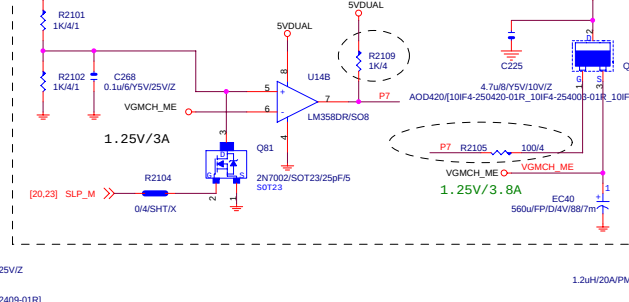
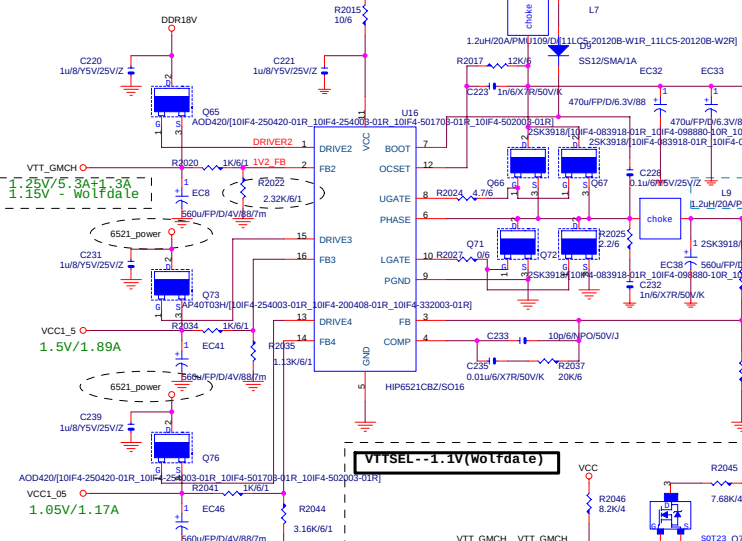
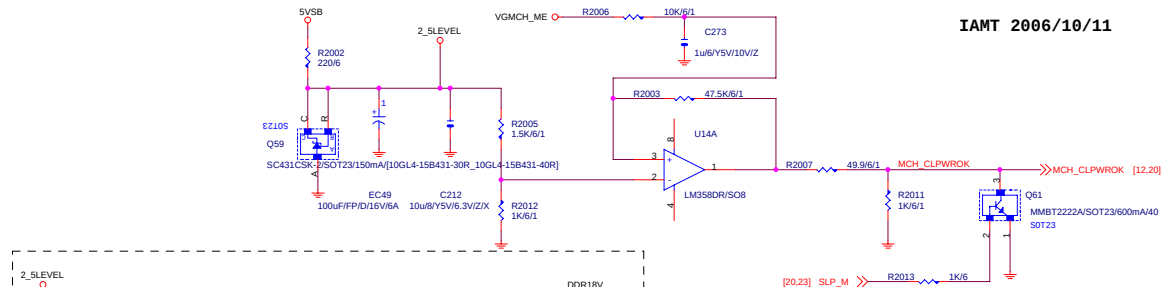
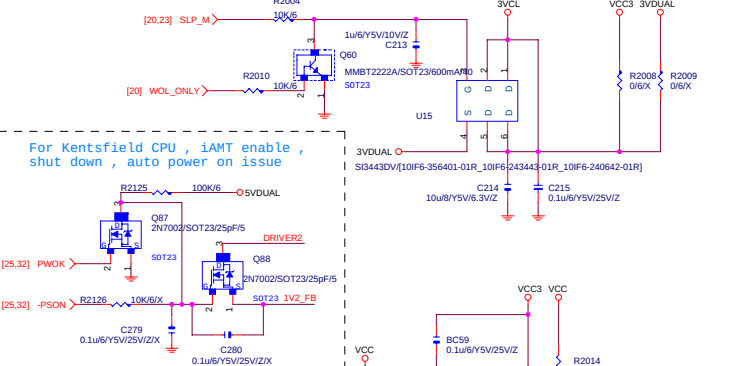
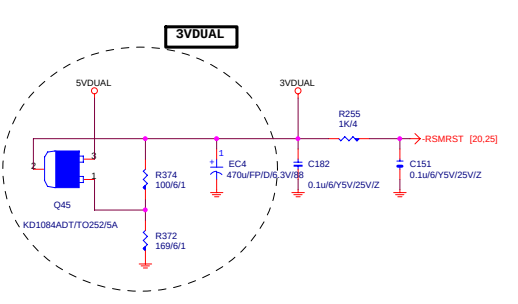
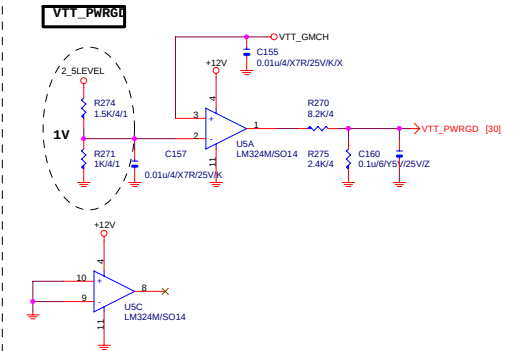
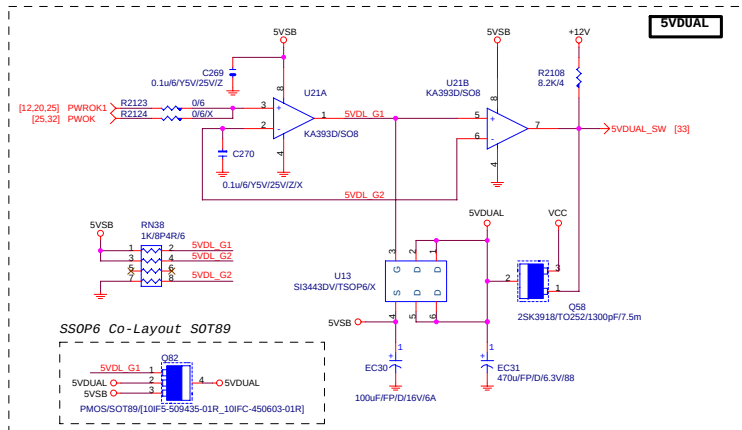


AZALIA FRONT PANEL



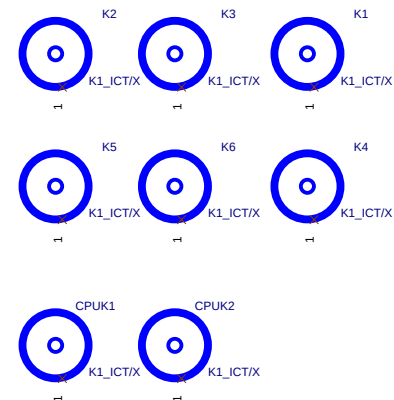
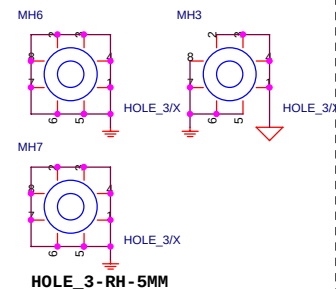
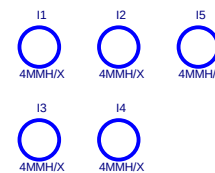
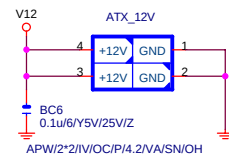
Gigabyte Technology

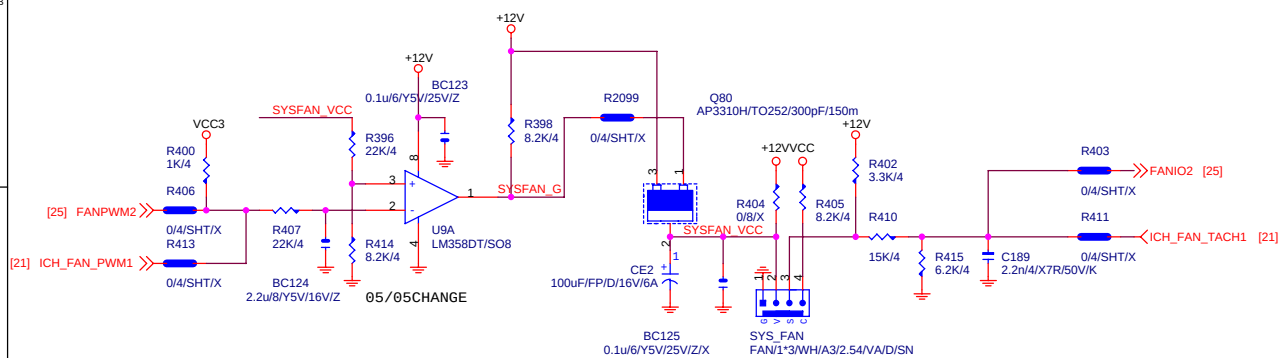
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Size			965QM-DS2
Date			Rev 2.0
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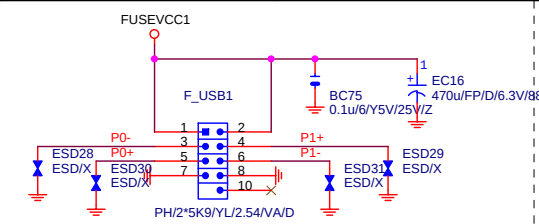
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Date:	2007-01-01		Sheet 31 of 36

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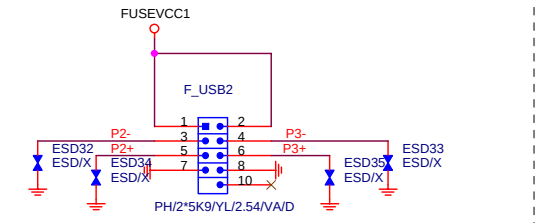
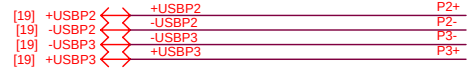




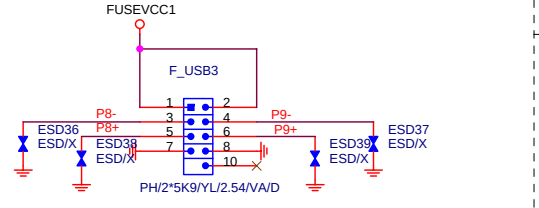
FRONT USB1



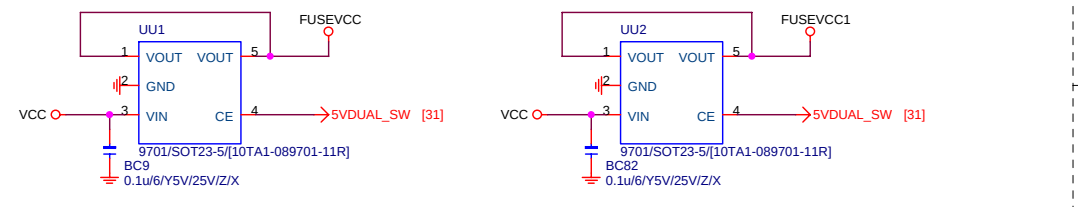
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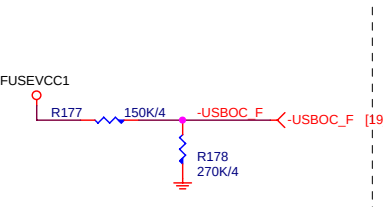
FRONT USB3



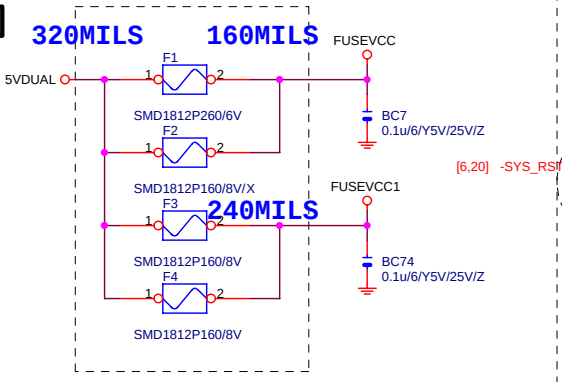
USB DROP



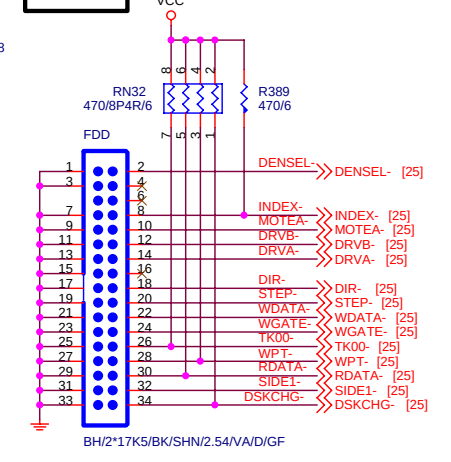
FRONT USB OC1



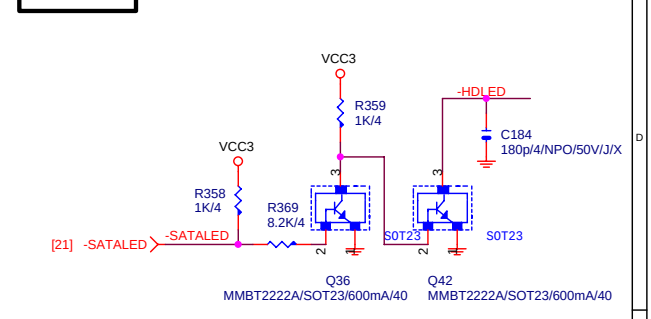
USB POWER



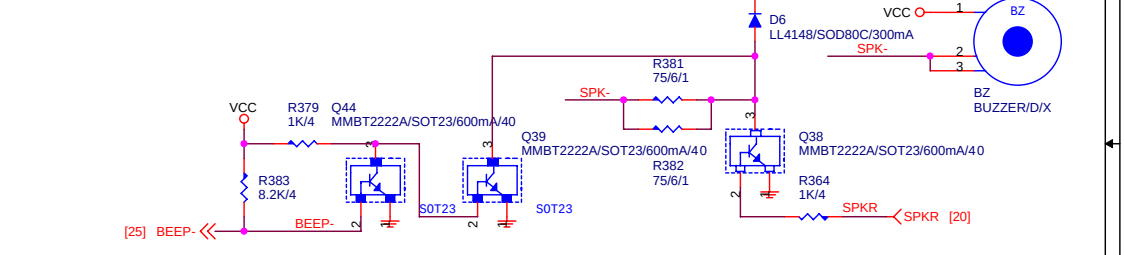
FLOPPY



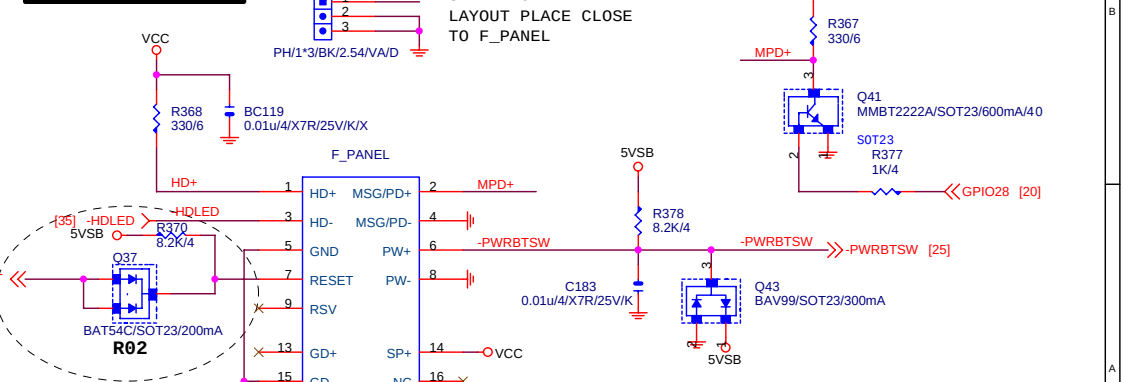
SATA LED



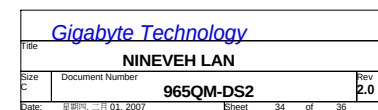
BUZZER



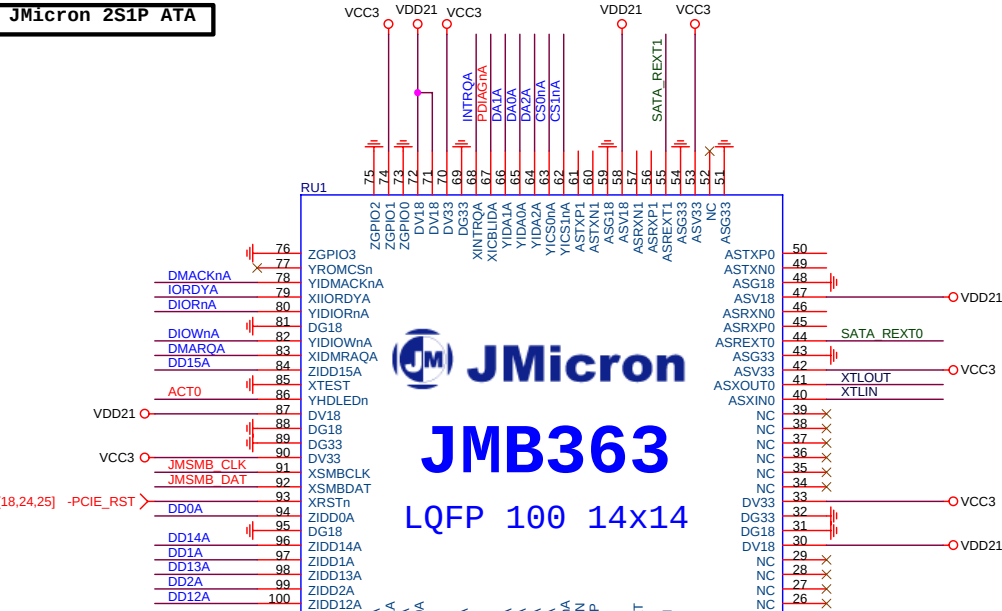
INTEL FRONT PANEL



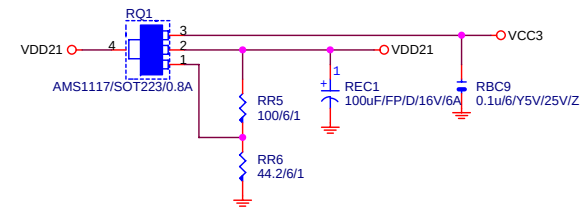
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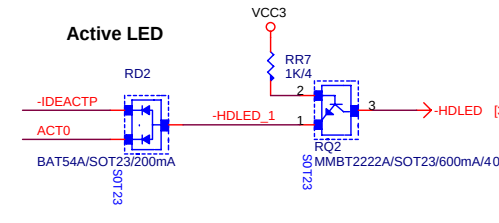
JMicron 2S1P ATA



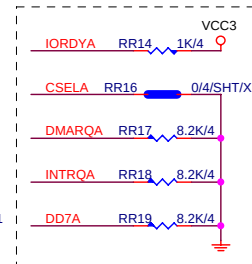
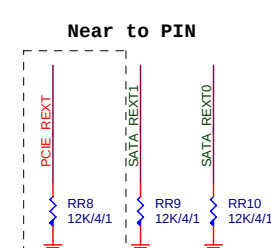
3.3V to 1.8V Voltage Regulator



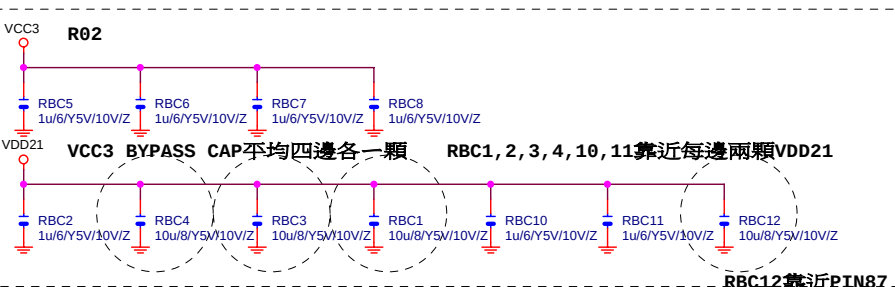
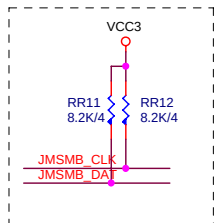
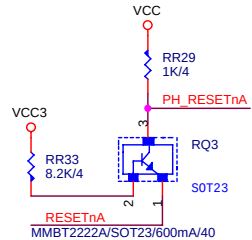
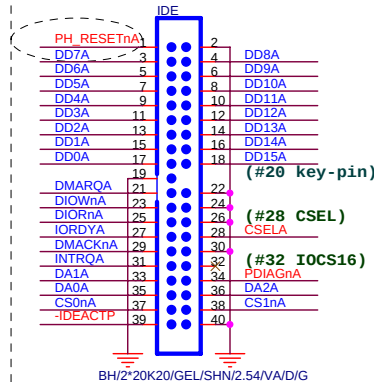
Active LED



Near to PIN



IDE Connector



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